



MICROCHIP

PCN#: LIAL-03HHRI927

CCB#: 3873

**Implement top marking changes for selected
ATMEGA480x, ATMEGA320x, ATMEGA160x,
ATMEGA80x Atmel device families manufactured with
59.91k process technology.**



Top Marking Pre and Post Change 48L TQFP

Pre Change

Atmel
MEGA4809
-AFR
YYWWR COO
YYWWNNN

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4	Line 5
ATMEGA4809-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA4809	<j>>AFR	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN
ATMEGA3209-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA3209	<j>>AFR	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN
ATMEGA1609-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA1609	<j>>AFR	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN
ATMEGA809-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA809	<j>>AFR	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN

Post Change

Atmel
MEGA4809
COO
YYWWNNN

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4809-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA4809	<j>><COO>	<j>>YYWWNNN
ATMEGA3209-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA3209	<j>><COO>	<j>>YYWWNNN
ATMEGA1609-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA1609	<j>><COO>	<j>>YYWWNNN
ATMEGA809-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA809	<j>><COO>	<j>>YYWWNNN

Top Marking Pre and Post Change 32L TQFP

Pre Change

Atmel
MEGA4808
-AFR
YYWWR COO
YYWWNNN

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4	Line 5
ATMEGA4808-AF/R	<Q1><O>< ><BS3><ATN>	< >MEGA4808	< >-AFR	< >YYWWR<ev1><BS1><COO>	< >YYWWNNN
ATMEGA3208-AF/R	<Q1><O>< ><BS3><ATN>	< >MEGA3208	< >-AFR	< >YYWWR<ev1><BS1><COO>	< >YYWWNNN
ATMEGA1608-AF/R	<Q1><O>< ><BS3><ATN>	< >MEGA1608	< >-AFR	< >YYWWR<ev1><BS1><COO>	< >YYWWNNN
ATMEGA808-AF/R	<Q1><O>< ><BS3><ATN>	< >MEGA808	< >-AFR	< >YYWWR<ev1><BS1><COO>	< >YYWWNNN

Post Change

Atmel
MEGA4808
COO
YYWWNNN

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4808-AF/R	<Q1><O>< ><BS3><ATN>	< >MEGA4808	< ><COO>	< >YYWWNNN
ATMEGA3208-AF/R	<Q1><O>< ><BS3><ATN>	< >MEGA3208	< ><COO>	< >YYWWNNN
ATMEGA1608-AF/R	<Q1><O>< ><BS3><ATN>	< >MEGA1608	< ><COO>	< >YYWWNNN
ATMEGA808-AF/R	<Q1><O>< ><BS3><ATN>	< >MEGA808	< ><COO>	< >YYWWNNN

Top Marking Pre and Post Change 32L VFQN

Pre Change

Atmel
M4808F
YYWWR COO
YYWWNNN

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4808-MF/R	<Q1><D><j><BS1><ATN>	<j>M4808F	<j>YYWW<rev1><BS1><CCO>	<j>YYWWNNN
ATMEGA3208-MF/R	<Q1><D><j><BS1><ATN>	<j>M3208F	<j>YYWW<rev1><BS1><CCO>	<j>YYWWNNN
ATMEGA1608-MF/R	<Q1><D><j><BS1><ATN>	<j>M1608F	<j>YYWW<rev1><BS1><CCO>	<j>YYWWNNN
ATMEGA808-MF/R	<Q1><D><j><BS1><ATN>	<j>M808F	<j>YYWW<rev1><BS1><CCO>	<j>YYWWNNN

Post Change

Atmel
M4808
COO
YYWWNNN

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4808-MF/R	<Q1><D><j><BS1><ATN>	<j>M4808	<j><CCO>	<j>YYWWNNN
ATMEGA3208-MF/R	<Q1><D><j><BS1><ATN>	<j>M3208	<j><CCO>	<j>YYWWNNN
ATMEGA1608-MF/R	<Q1><D><j><BS1><ATN>	<j>M1608	<j><CCO>	<j>YYWWNNN
ATMEGA808-MF/R	<Q1><D><j><BS1><ATN>	<j>M808	<j><CCO>	<j>YYWWNNN



Top Marking Pre and Post Change 48L UQFN

Pre Change

Atmel
MEGA4809
MFR COO
YYWWNNN

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4809-MF/R	<Q1><O><j><BS1><ATN>	<j>MEGA4809	<j>MFR<BS3><COO>	<j>YYWWNNN
ATMEGA3209-MF/R	<Q1><O><j><BS1><ATN>	<j>MEGA3209	<j>MFR<BS3><COO>	<j>YYWWNNN

Post Change

Atmel
MEGA4809
COO
YYWWNNN

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4809-MF/R	<Q1><O><j><BS1><ATN>	<j>MEGA4809	<j><COO>	<j>YYWWNNN
ATMEGA3209-MF/R	<Q1><O><j><BS1><ATN>	<j>MEGA3209	<j><COO>	<j>YYWWNNN



Top marking Pre and Post Change 28L SSOP

Pre Change

Atmel
MEGA4808-XFR
YYWWR COO
YYWWNNN

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4808-XF/R	<j><BS2><ATN>	<j>MEGA4808-XFR	<j><BS2>YYWW<rev1><BS1><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA3208-XF/R	<j><BS2><ATN>	<j>MEGA3208-XFR	<j><BS2>YYWW<rev1><BS1><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA1608-XF/R	<j><BS2><ATN>	<j>MEGA1608-XFR	<j><BS2>YYWW<rev1><BS1><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA808-XF/R	<j><BS2><ATN>	<j>MEGA808-XFR	<j><BS2>YYWW<rev1><BS1><COO>	<Q3><Q><BS2><j>YYWWNNN

Post Change

Atmel
MEGA4808
COO
YYWWNNN

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4808-XF/R	<j><BS2><ATN>	<j>MEGA4808	<j><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA3208-XF/R	<j><BS2><ATN>	<j>MEGA3208	<j><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA1608-XF/R	<j><BS2><ATN>	<j>MEGA1608	<j><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA808-XF/R	<j><BS2><ATN>	<j>MEGA808	<j><COO>	<Q3><Q><BS2><j>YYWWNNN



Top Marking Pre and Post Change 40L PDIP

Pre Change

ATMEGA4809-PF
XXXXXXX
Atmel

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4809-PF/R	<jc><bl>	<jc>ATMEGA4809-PF	<jc>YYWWNNN	<Q3><O><jc><ATN>

Post Change

ATMEGA4809
XXXXXXX
Atmel

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4809-PF/R	<jc><bl>	<jc>ATMEGA4809	<jc>YYWWNNN	<Q3><O><jc><ATN>