

Integrated Software Framework v2.06

STANDARD FEATURES

- MPLAB® Harmony is a flexible, abstracted, fully integrated firmware development platform for PIC32 microcontrollers
- Broad range of Middleware Stack/Libraries, including: USB, TCP/IP, Wi-Fi™, File System, Graphics, Bootloaders, Bluetooth™, Audio, DSP, Math, Cryptography, Drivers, System Services, and more
- Over 160 Application Demonstrations with up to 600 application configurations to accelerate application development
- Seamlessly integrates third-party solutions (RTOS, Middleware, Drivers, etc.) into the software framework
- RTOS support, which includes: FreeRTOS™, OPENRTOS, Express Logic Thread X, SEGGER embOS®, Micrium® µC/OS-II™, Micrium µC/OS-III™
- Middleware support, which includes: SEGGER emWin®, InterNiche Technologies, Inc., wolfSSL, and PubNub®
- Both free and enabling license terms provided

For a detailed list of features, please visit the MPLAB Harmony Web page at:

www.microchip.com/harmony

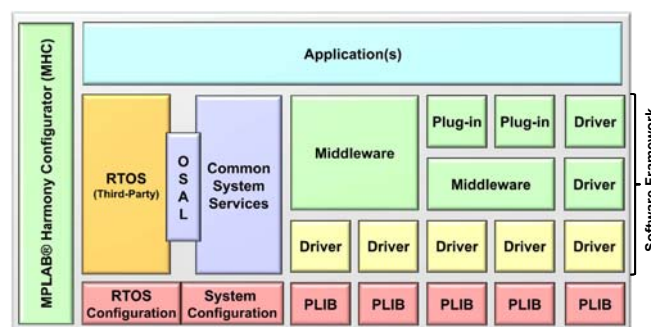
From the landing page, scroll down and select the **Features** tab.

DESCRIPTION

MPLAB Harmony is a flexible, abstracted, fully integrated firmware development platform for PIC32 microcontrollers. MPLAB Harmony's modular architecture allows drivers and libraries to work together with minimal effort. It is scalable across PIC32 Microchip devices to custom fit customers' requirements.

MPLAB Harmony takes key elements of modular and object oriented design, adding an Operating System Abstraction Layer (OSAL) that provides the flexibility to use a Real-Time Operating System (RTOS) or work without one, and provides a framework of software modules that are easy to use, configurable for your specific needs, and that work together in complete harmony.

In addition, the MPLAB Harmony Configurator (MHC) and code development format allows for maximum reuse and reduces time to market.



COMPLIANCE

Compliant with MISRA-C:2012 Mandatory Standards:

- MPLAB Harmony Peripheral Libraries
- TCP/IP Library

DEVELOPMENT TOOLS

- MPLAB X IDE v4.20 is required
- MPLAB XC32 C/C++ Compiler v2.10 (ISO 26262)
- MPLAB X IDE plug-ins:
 - MPLAB Harmony Configurator (MHC) v2.0.6.0

THIRD-PARTY DEVELOPERS

Microchip offers a range of documentation to assist you with the design of your own software offerings for MPLAB® Harmony. These documents, which are provided with the installation Help, are also available for download from the MPLAB Harmony website (see ["Download Information"](#) for details).

- MPLAB Harmony Overview
- MPLAB Harmony Compatibility Guide
- MPLAB Harmony Creating Your First Project Tutorial
- MPLAB Harmony Driver Development Guide
- MPLAB Harmony Configurator User's Guide
- MPLAB Harmony Graphics Composer User's Guide
- MPLAB Harmony Test Harness User's Guide

MPLAB HARMONY v2.06

v2.06 FEATURE UPDATES AND ADDITIONS

MPLAB Graphics Composer:

- Added string table import / export
- Added scheme import / export
- Added DDR memory manager
- Improved accuracy of heap estimator
- Added new widgets to toolbox

Aria Graphics User Interface Library:

- Added arc drawing primitive
- Added circular gauge widget
- Added circular slider widget
- Added bar graph widget
- Added line graph widget
- Added pie chart widget
- Added image plus widget
- Added radial menu widget
- Improved list wheel widget
- Improved keypad widget
- Added multi-line text support to widgets
- Improved RTOS integration (PIC32MZ DA)
- Improved GPU driver performance (PIC32MZ DA)

Touch Functionality:

- Added multi-finger gesture support
- Added Input system service (performance enhancement)
- Deprecated touch system service
- Added resistive touch to input system service
- Added resistive touch calibration example

New Applications:

- Aria Adventure graphic (parallax animation)
- Aria Image Viewer graphics (2-finger pinch gesture/zoom)
- Aria Showcase Reloaded (new widget examples)
- Aria Radial Menu (radial menu views)
- Aria Touch ADC Calibrate
- Real-time FFT (audio inputs, FFT DSP, display)
- Smart speaker (echo cancellation example)
- Data voice control (Bluetooth SPP to Google voice cloud recognition)
- Example for third party display port (external control / third party touch)
- Resistive touch calibration
- Added Speex encoder to universal audio encoders

Audio:

- Added examples for Google voice
- Added example for acoustic echo cancellation
- Updated AK4954 audio codec driver
- Updates PIC32MZ DSP fractional math library
- Added Speex encoder API

MPLAB Harmony TCP/IP Stack:

- TFTP server support added to the stack
- Added ICMP support for broadcast pings
- Added two new FTP commands - DELETE and NOOP
- Added announce console command
- Added heap high watermark functionality
- The mail SMTP module has been added to the standard demonstrations. The old SMTP module has been marked as deprecated.

New Tutorial:

- Creating a MPLAB Harmony Graphics Application Using a Third-Party Display.

New Example:

- Graphics Event Testbed (./apps/examples/events_testbed)
- Remote Device Symmetric Key Authentication with secure element ATECC608A. (./apps/crypto/ecc_symmetric)
- Remote Device Asymmetric Key Authentication with secure element ATECC608A. (./apps/crypto/ecc_asymmetric)

DOWNLOAD INFORMATION

MPLAB Harmony, including the current release notes and Software License Agreement, is available for download by visiting:

<http://www.microchip.com/mplabharmony>

ADDITIONAL RESOURCES

MPLAB Harmony TV offers a wide range of getting started and training videos. The video content is available by scrolling to the bottom of the MPLAB Harmony webpage at:

<http://www.microchip.com/mplabharmony>

The **Microchip Developer Site** provides short introductory videos, self-paced training modules, and answers to frequently asked questions.

<http://microchip.wikidot.com/harmony:start>

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the Microchip products in a manner outside the operating specifications contained in Microchip's Data Sheets. Most likely, the person doing so is engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as "unbreakable."

Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our products. Attempts to break Microchip's code protection feature may be a violation of the Digital Millennium Copyright Act. If such acts allow unauthorized access to your software or other copyrighted work, you may have a right to sue for relief under that Act.

Information contained in this publication regarding device applications and the like is provided only for your convenience and may be superseded by updates. It is your responsibility to ensure that your application meets with your specifications. MICROCHIP MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE INFORMATION, INCLUDING BUT NOT LIMITED TO ITS CONDITION, QUALITY, PERFORMANCE, MERCHANTABILITY OR FITNESS FOR PURPOSE. Microchip disclaims all liability arising from this information and its use. Use of Microchip devices in life support and/or safety applications is entirely at the buyer's risk, and the buyer agrees to defend, indemnify and hold harmless Microchip from any and all damages, claims, suits, or expenses resulting from such use. No licenses are conveyed, implicitly or otherwise, under any Microchip intellectual property rights unless otherwise stated.

Microchip received ISO/TS-16949:2009 certification for its worldwide headquarters, design and wafer fabrication facilities in Chandler and Tempe, Arizona; Gresham, Oregon and design centers in California and India. The Company's quality system processes and procedures are for its PIC® MCUs and dsPIC® DSCs, KEELoQ® code hopping devices, Serial EEPROMs, microperipherals, nonvolatile memory and analog products. In addition, Microchip's quality system for the design and manufacture of development systems is ISO 9001:2000 certified.

QUALITY MANAGEMENT SYSTEM
CERTIFIED BY DNV
== ISO/TS 16949 ==

Trademarks

The Microchip name and logo, the Microchip logo, AnyRate, AVR, AVR logo, AVR Freaks, BeaconThings, BitCloud, CryptoMemory, CryptoRF, dsPIC, FlashFlex, flexPWR, Helder, JukeBlox, KEELoQ, KEELoQ logo, Klear, LANCheck, LINK MD, maxStylus, maxTouch, MediaLB, megaAVR, MOST, MOST logo, MPLAB, OptoLyzer, PIC, picoPower, PICSTART, PIC32 logo, Prochip Designer, QTouch, RightTouch, SAM-BA, SpyNIC, SST, SST Logo, SuperFlash, tinyAVR, UNI/O, and XMEGA are registered trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

ClockWorks, The Embedded Control Solutions Company, EtherSynch, Hyper Speed Control, HyperLight Load, IntelliMOS, mTouch, Precision Edge, and Quiet-Wire are registered trademarks of Microchip Technology Incorporated in the U.S.A.

Adjacent Key Suppression, AKS, Analog-for-the-Digital Age, Any Capacitor, AnyIn, AnyOut, BodyCom, chipKIT, chipKIT logo, CodeGuard, CryptoAuthentication, CryptoCompanion, CryptoController, dsPICDEM, dsPICDEM.net, Dynamic Average Matching, DAM, ECAN, EtherGREEN, In-Circuit Serial Programming, ICSP, Inter-Chip Connectivity, JitterBlocker, KlearNet, KlearNet logo, Mindi, MiWi, motorBench, MPASM, MPF, MPLAB Certified logo, MPLIB, MPLINK, MultiTRAK, NetDetach, Omniscient Code Generation, PICDEM, PICDEM.net, PICKit, PICtail, PureSilicon, QMatrix, RightTouch logo, REAL ICE, Ripple Blocker, SAM-ICE, Serial Quad I/O, SMART-I.S., SQI, SuperSwitcher, SuperSwitcher II, Total Endurance, TSHARC, USBCheck, VariSense, ViewSpan, WiperLock, Wireless DNA, and ZENA are trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

SQTP is a service mark of Microchip Technology Incorporated in the U.S.A.

Silicon Storage Technology is a registered trademark of Microchip Technology Inc. in other countries.

GestIC is a registered trademark of Microchip Technology Germany II GmbH & Co. KG, a subsidiary of Microchip Technology Inc., in other countries.

All other trademarks mentioned herein are property of their respective companies.

© 2017, Microchip Technology Incorporated, All Rights Reserved.
ISBN: