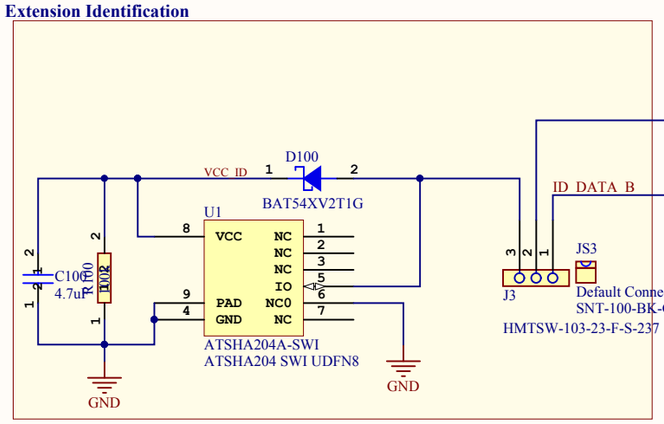
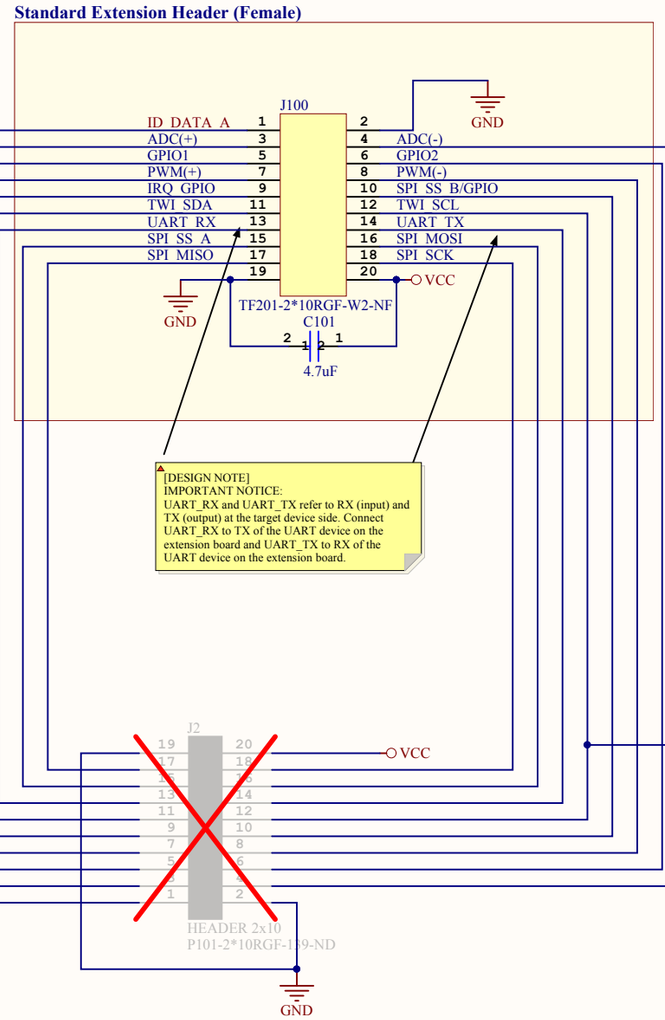
Extension Identification

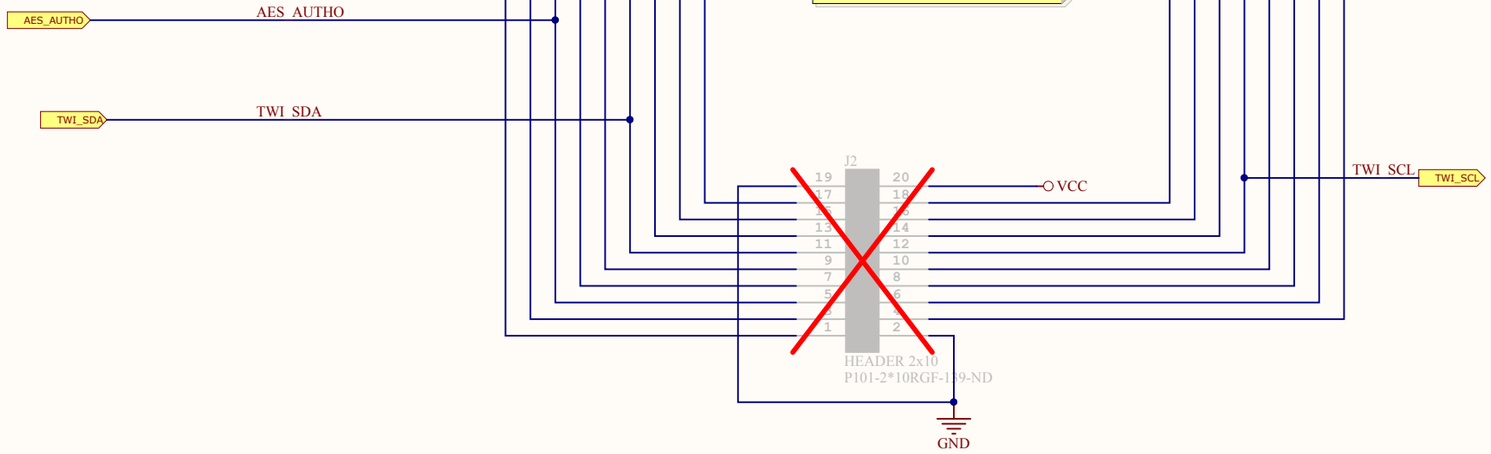


[DESIGN NOTE]
The ATSHA204 is powered through an IO Pin from the EDBG on an MCU board.
R100 is present to drain the capacitor C100 when power is removed from an MCU board.

Standard Extension Header (Female)



[DESIGN NOTE]
IMPORTANT NOTICE:
UART_RX and UART_TX refer to RX (input) and TX (output) at the target device side. Connect UART_RX to TX of the UART device on the extension board and UART_TX to RX of the UART device on the extension board.



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1150 E. Cheyenne Mtn Blvd			
Colorado Springs *			
Colorado, USA			
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Document number:	*		Revision 1
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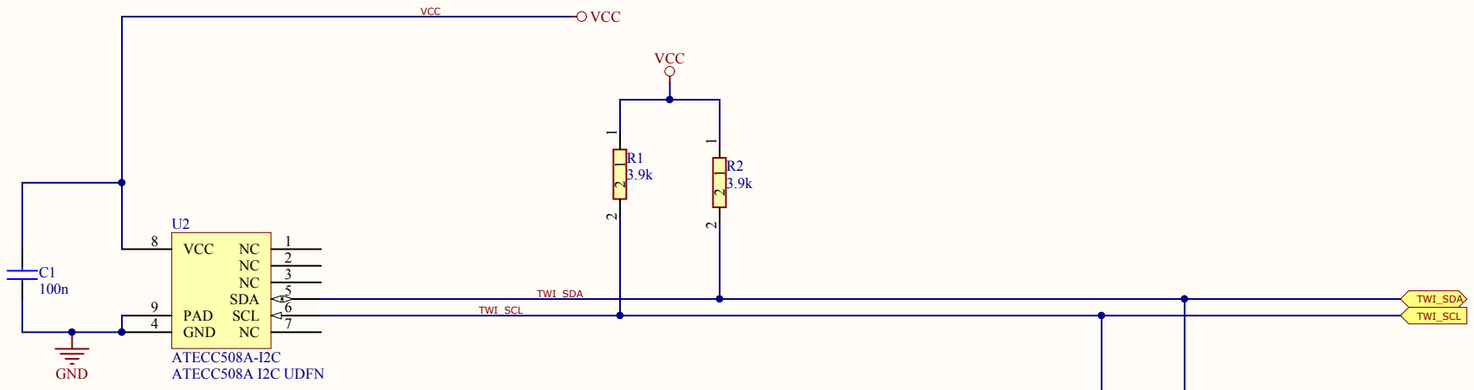
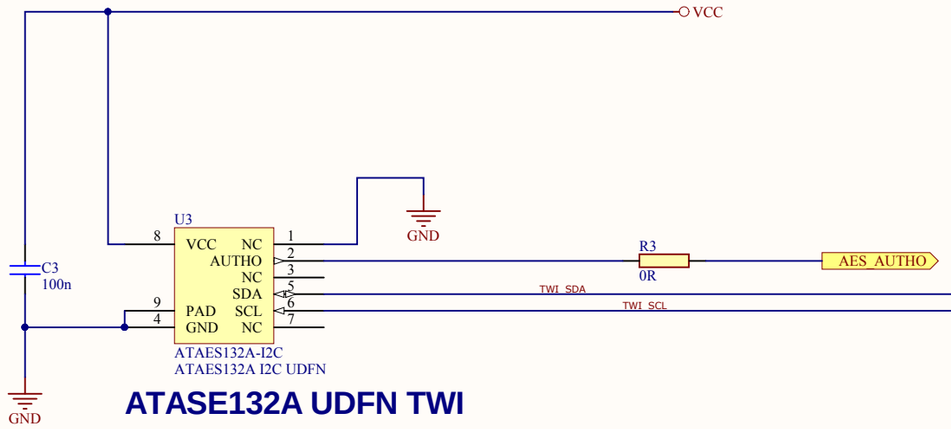
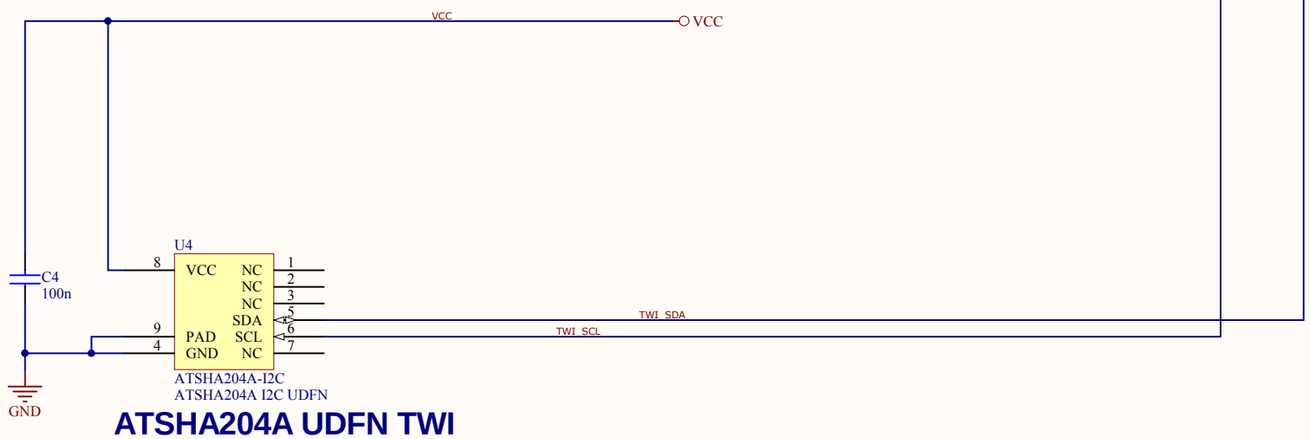
CryptoAuth_Xplained_Pro_Connector.SchDoc			



ATECC508A UDFN TWI



ATASE132A UDFN TWI



ATSHA204A UDFN TWI

F1
FIDUCIAL
FIDUCIAL 1.5mm

F2
FIDUCIAL
FIDUCIAL 1.5mm

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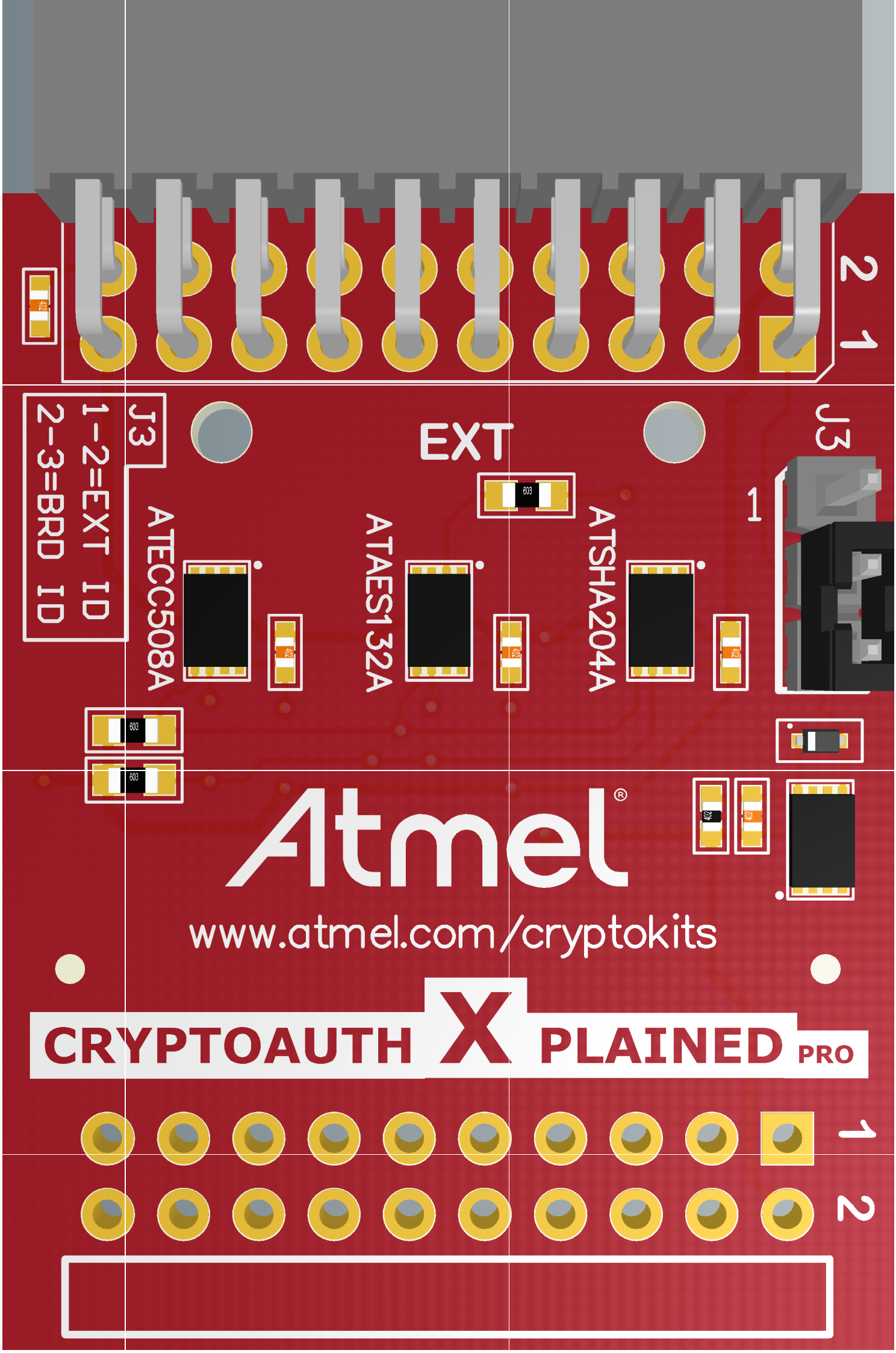
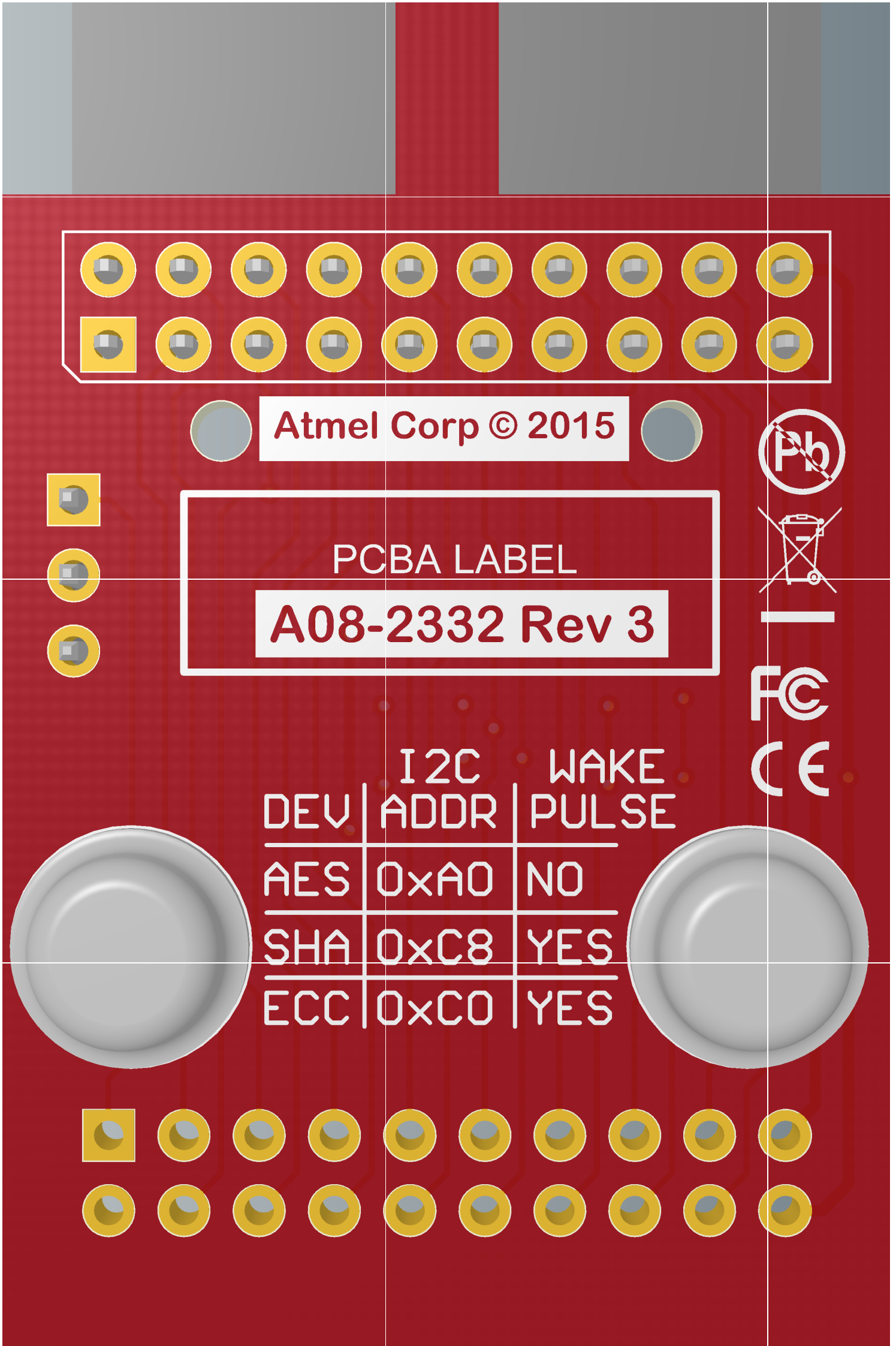
Colorado Springs	*		
Colorado USA			
Date:	4/27/2015	5:41:31 PM	PAGE: 3 of 3
Document number:	*		Revision 1
TITLE: Crypto Device Schematic			
CryptoAuth_Xplained_Pro_CRYPTO.SchDoc			


2
1
J3
1-2=EXT ID
2-3=BRD ID
EXT
ATECC508A
ATAES132A
ATSHA204A
J3
1
Atmel
www.atmel.com/cryptokits
CRYPTOAUTH XPLAINED PRO
1
2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PCBA LABEL
A08-2332 Rev 3
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WAKE
DEV
ADDR
PULSE
AES
0xA0
NO
SHA
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YES
ECC
0xC0
YES