









DEBUG  
J24

16  
J18

2  
J22

J-Link Technology  
www.segger.com

FB13

VBUS\_JLINK

4 LAYERS

5V DC

GND

AN RST CS SCK MISO MOSI +3.3V GND

mikroBUS  
J14

GND

R150 R148

C7 C4

VBUS\_USBA

VBUS\_USBC

VBUS\_USBB

USBA

DISABLE BOOT  
Q11  
R189 Q12 R188 R187  
GREEN = BOOT ENABLED  
RED = BOOT DISABLED

12-bit ISI connector  
Camera should be facing outside

MICROCHIP

SAM9X60-EK  
EVALUATION KIT rev. 3

RoHS

CE

CE

CE

SD Card

Wi-Fi/BLE

USER PUSH BUTTONS

USER\_BTN SW1  
WAKE\_UP SW2  
nRST SW3  
DIS\_BOOT SW4

TXD  
RXD  
GND

VDD\_WLNC

C128 C127 C126 C125 C124 C123 C122 C121 C120 C119 C118 C117 C116 C115 C114 C113 C112 C111 C110 C109 C108 C107 C106 C105 C104 C103 C102 C101 C100 C99 C98 C97 C96 C95 C94 C93 C92 C91 C90 C89 C88 C87 C86 C85 C84 C83 C82 C81 C80 C79 C78 C77 C76 C75 C74 C73 C72 C71 C70 C69 C68 C67 C66 C65 C64 C63 C62 C61 C60 C59 C58 C57 C56 C55 C54 C53 C52 C51 C50 C49 C48 C47 C46 C45 C44 C43 C42 C41 C40 C39 C38 C37 C36 C35 C34 C33 C32 C31 C30 C29 C28 C27 C26 C25 C24 C23 C22 C21 C20 C19 C18 C17 C16 C15 C14 C13 C12 C11 C10 C9 C8 C7 C6 C5 C4 C3 C2 C1

GND  
GPIO17  
GPIO18  
GPIO19  
GPIO20  
SDA  
SCL  
3V3



The LCD should be mounted on top of the board and the ribbon cable should cover the ISI connector.  
The LCD interface and the Image Sensor Interface are mutually exclusive.  
You can use either one or the other at one time.

LABEL1

mikro  
BUS

AN RST CS SCK MISO MOSI +3.3V GND  
PAINT INT RX TX SCL SDA INT +5V GND

C143

