

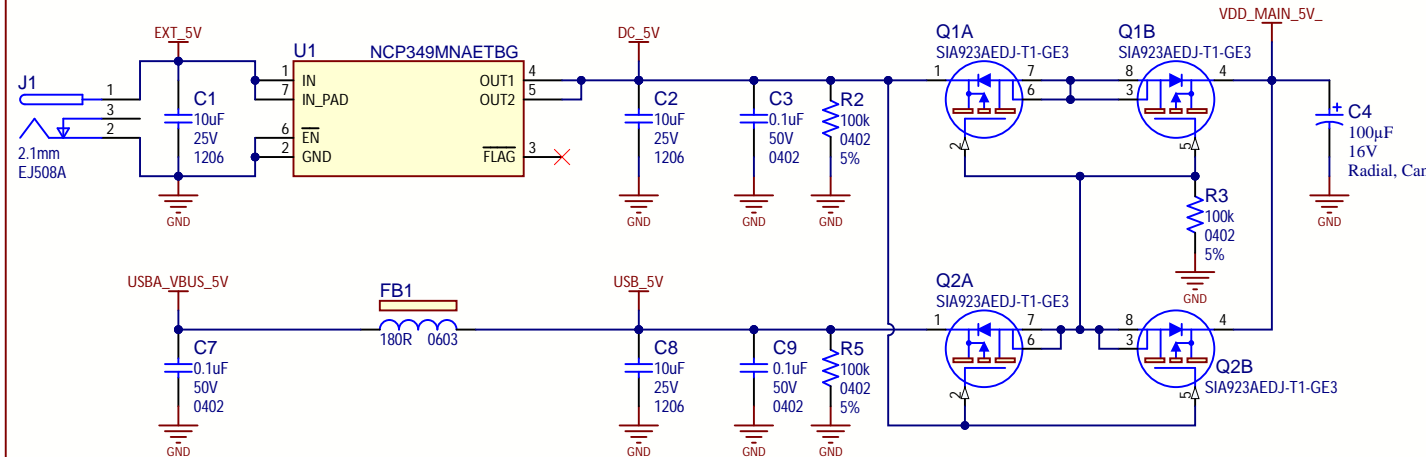
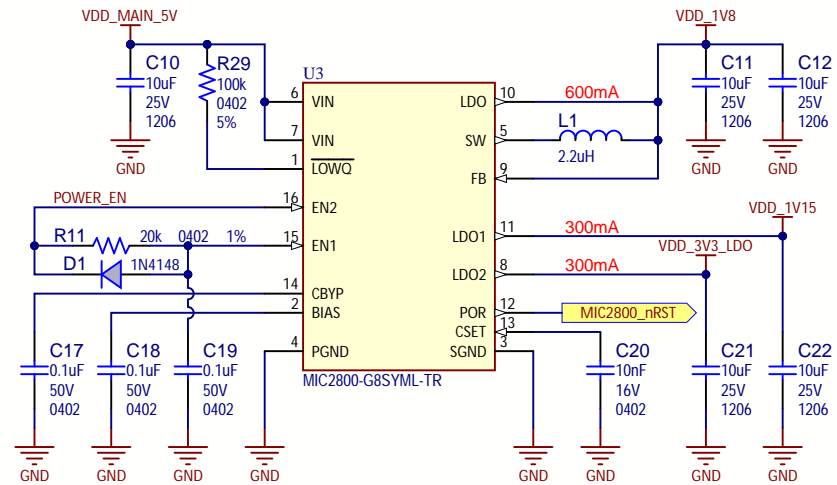


Drawn By: IGN		 <b>MICROCHIP</b>	
Engineer: IGN			
PartNumber: DT100126		Project Title <i>SAM9X60 Evaluation Kit</i>	
Sheet Title Block Diagram		Variant Name Standard_WILC3000_DNP	
Size B	Sch #: 03-11039	Date: 27.05.2022 10:17:20	 <a href="http://Altium.com">Altium.com</a>
	Revision: 3	Sheet 1 of 13	
File: 01 - Block Diagram.SchDoc			

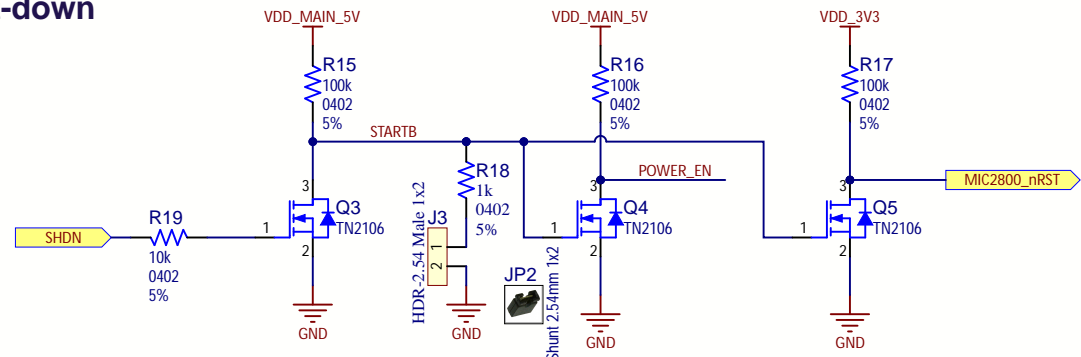
# Input Power Options



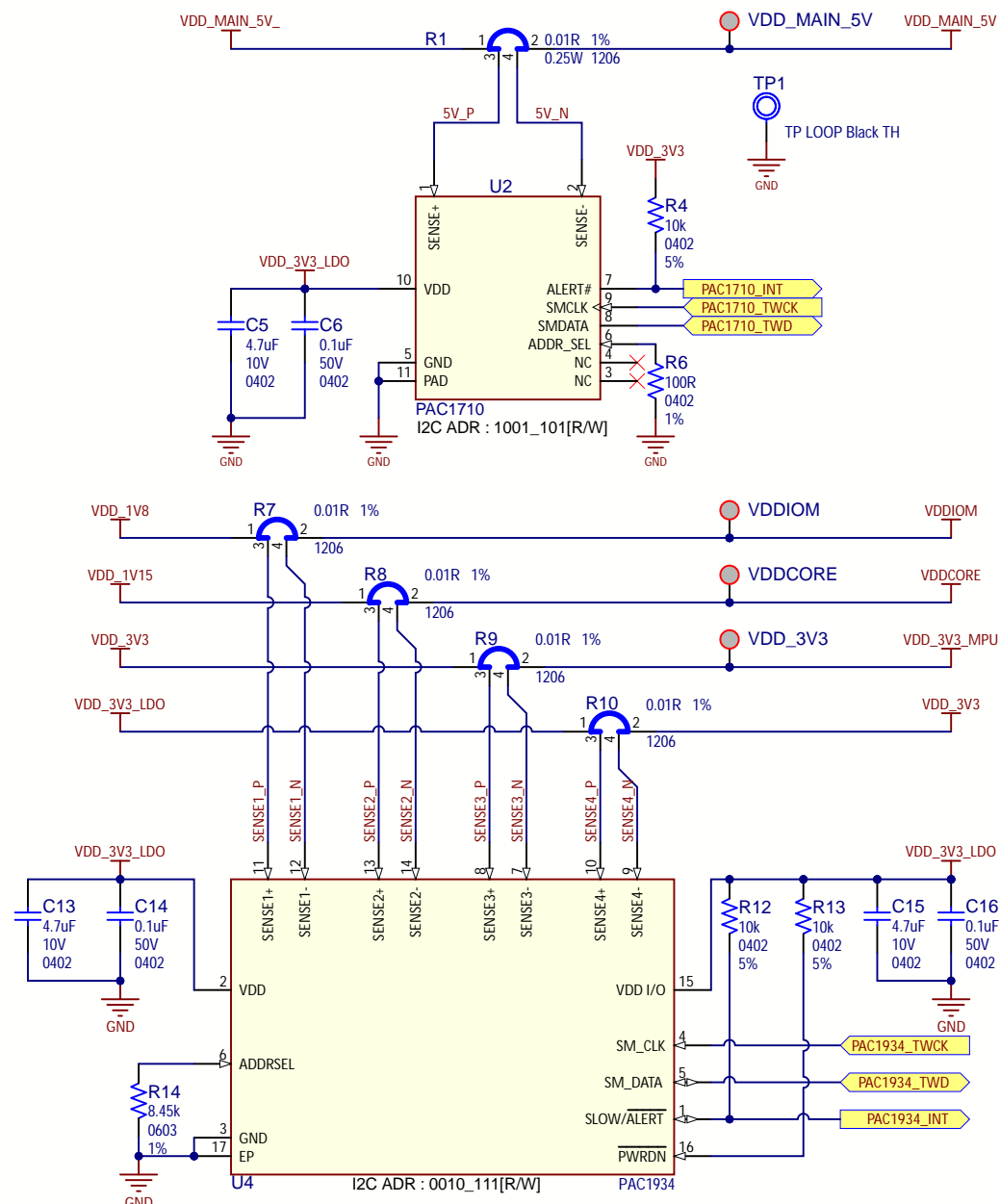
# PMIC



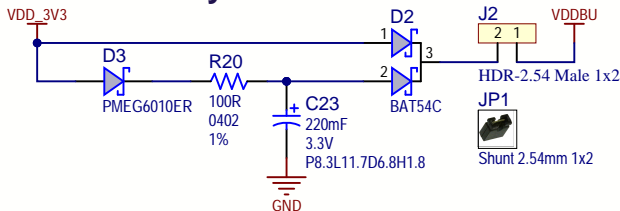
# Shut-down





# Power Measurement

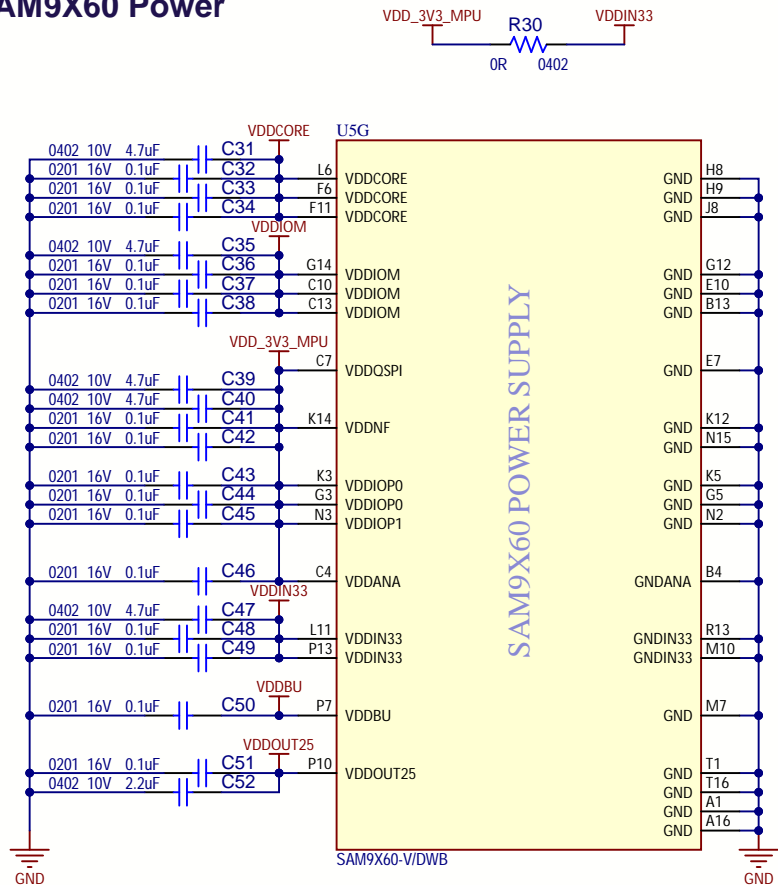


# Battery Unit



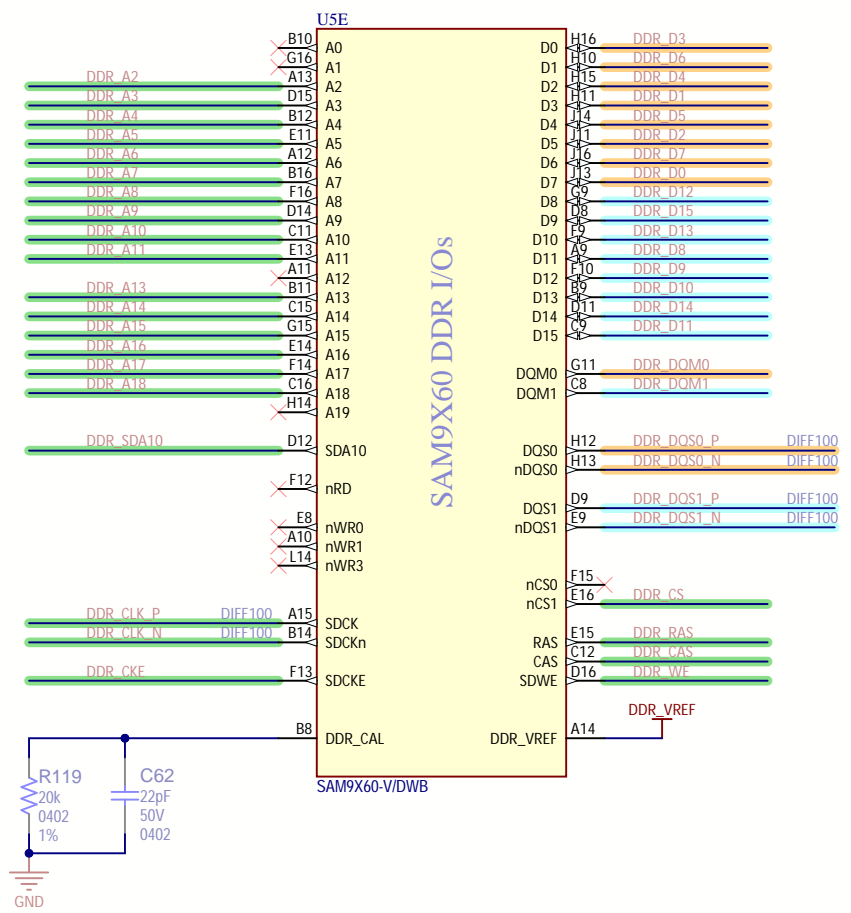
Drawn By: IGn	 <b>MICROCHIP</b>	
Engineer: IGn		
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>	
Sheet Title <b>Power supply</b>	Variant Name <b>Standard_WILC3000_DNP</b>	Designed with  <b>Altium</b>
Size B	Sch #: 03-11039	Date: 27.05.2022 10:17:20
	Revision:3	Sheet 2 of 13
File: 02 - Power supply.SchDoc		

SAM9X60 Power

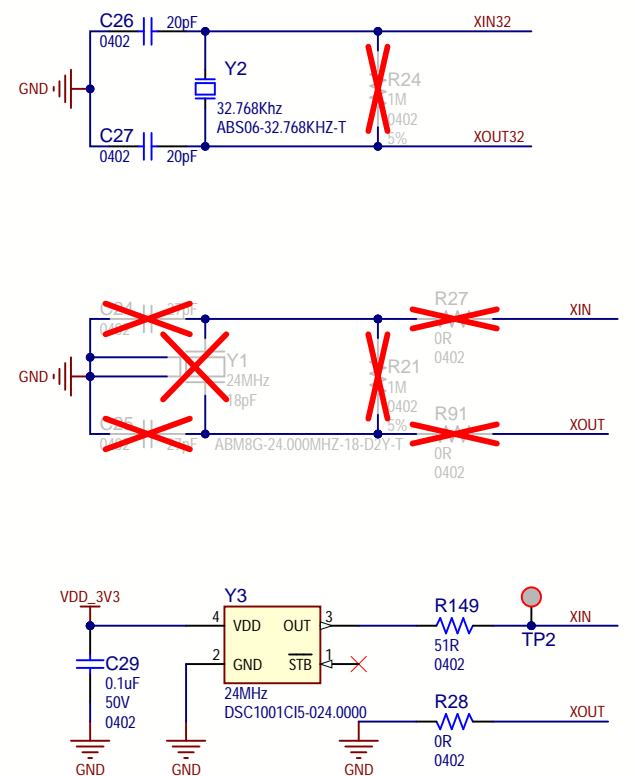


Decoupling caps should be placed as close as possible to their corresponding power balls  
Place C19 close to L11 or P13, and place C30 close to P10

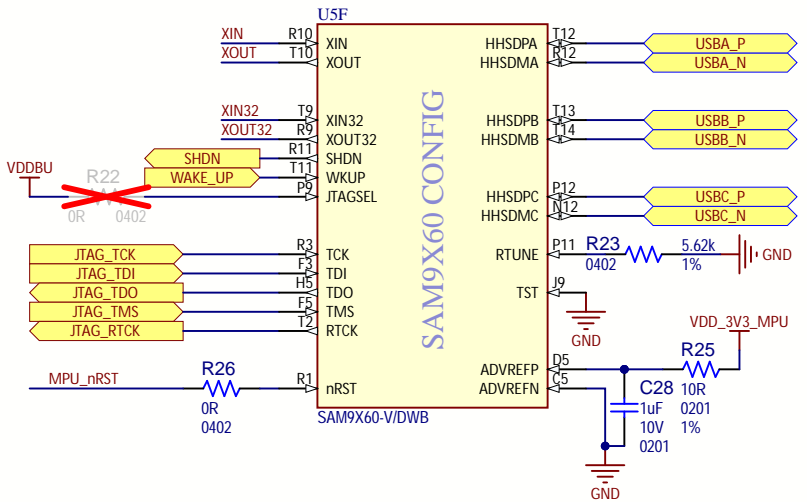
SAM9X60 DDR Controller



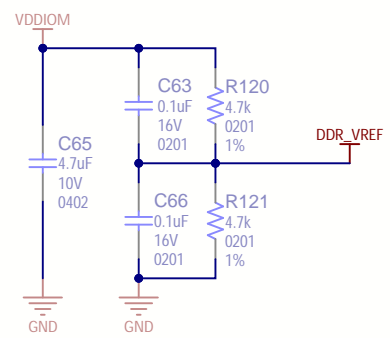
SAM9X60 Clocks



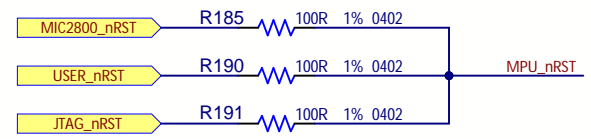
SAM9X60 Core





DDR\_VREF

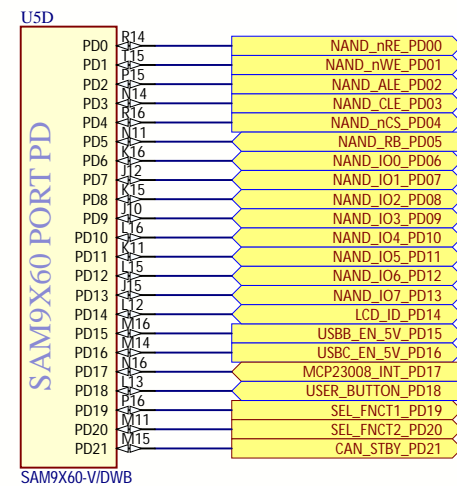
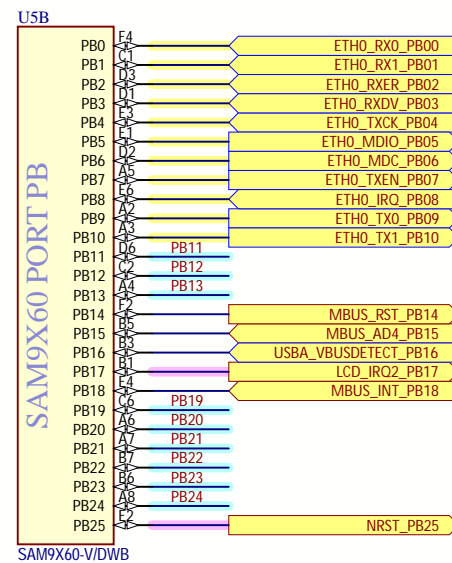
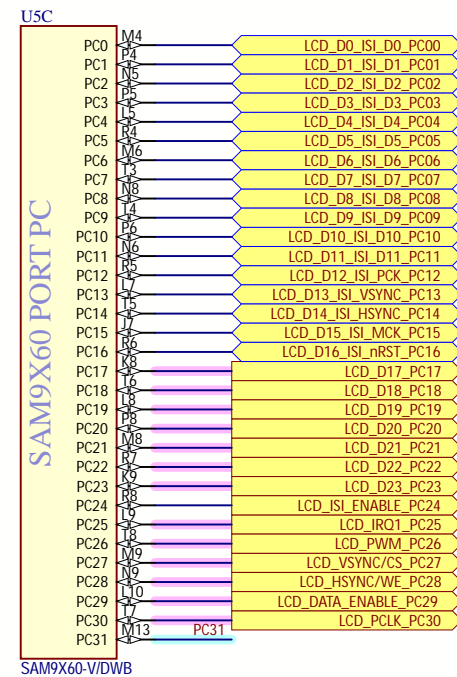
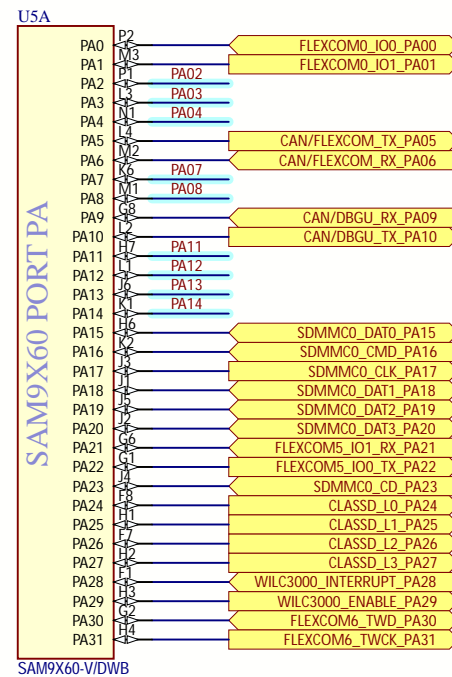


SAM9X60 Reset

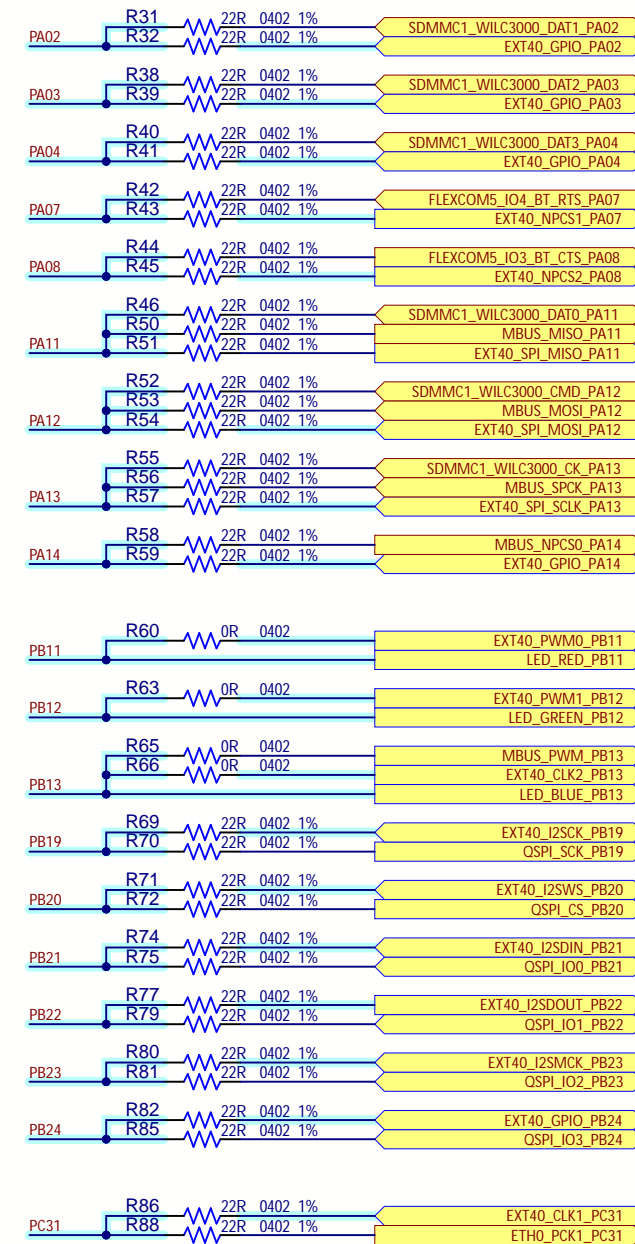


Drawn By: lGn		
Engineer: lGn		
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>	
Sheet Title Processor SAM9X60	Variant Name Standard_WILC3000_DNP	Designed with  Altium.com
Size B	Sch #: 03-11039	
Revision: 3	Date: 27.05.2022 10:17:20	
File: 03 - Processor SAM9X60.SchDoc		Sheet 3 of 13

## SAM9X60 PIOs



## SAM9X60 PIO Muxing



Drawn By:  
IGN

Engineer:  
IGn

PartNumber:  
DT100126

Sheet Title
Processor IOs

Size	B
------	---

File: 04 - Processor IOs.SchDoc



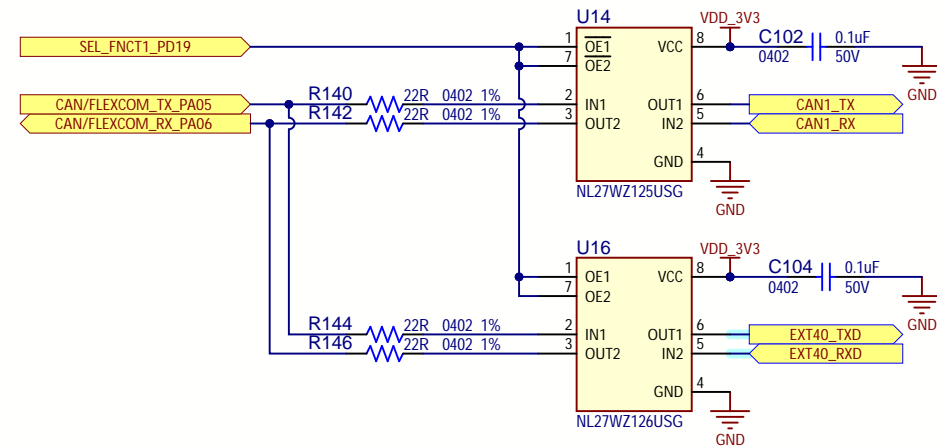
Project Title	<i>SAM9X60 Evaluation Kit</i>
---------------	-------------------------------

Variant Name	Standard_WILC3000_DNP
--------------	-----------------------

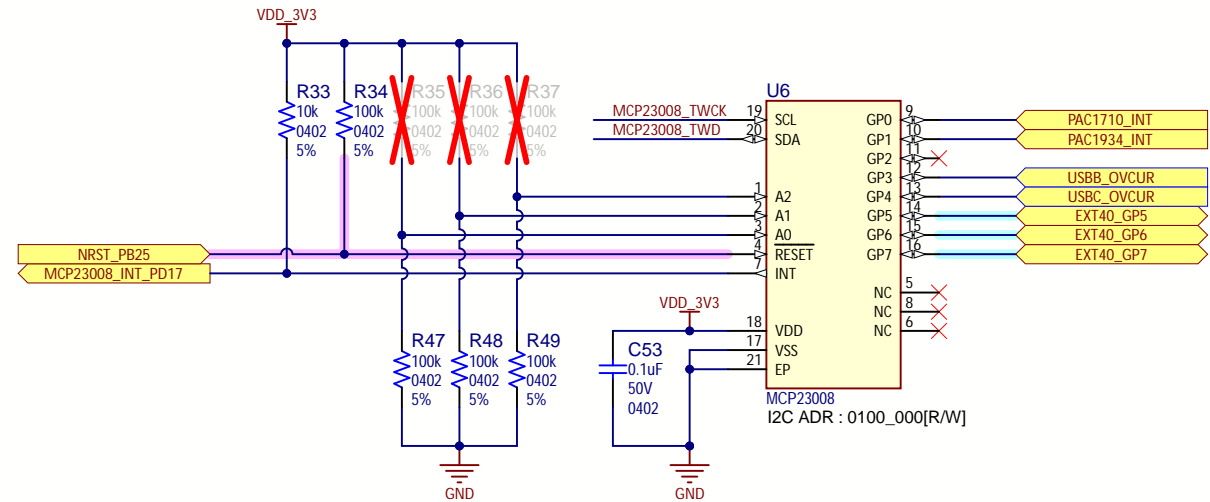
Date: 27.05.2022 10:17:20  
Sheet 4 of 13

Designed with  
**Altium**  
[Altium.com](http://Altium.com)

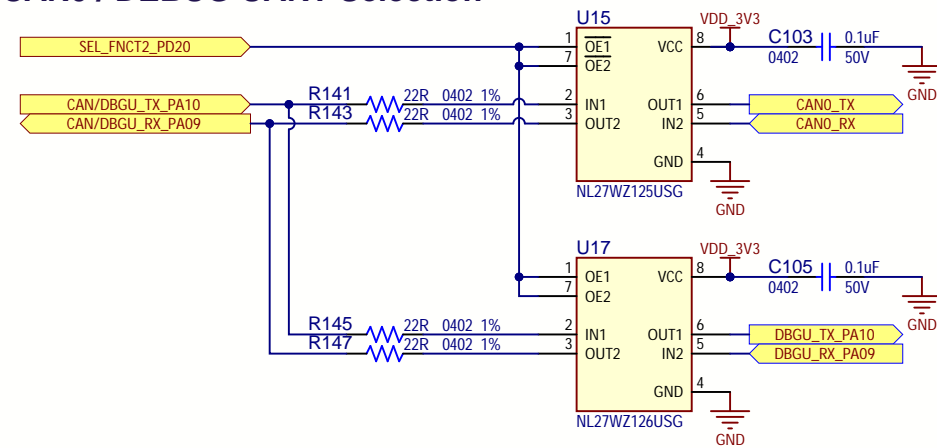
### CAN1 / EXT40 UART Selection



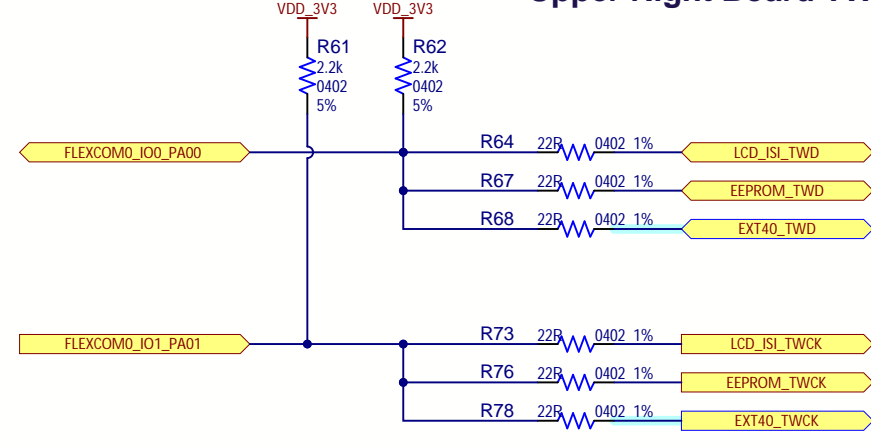
### Port Expander



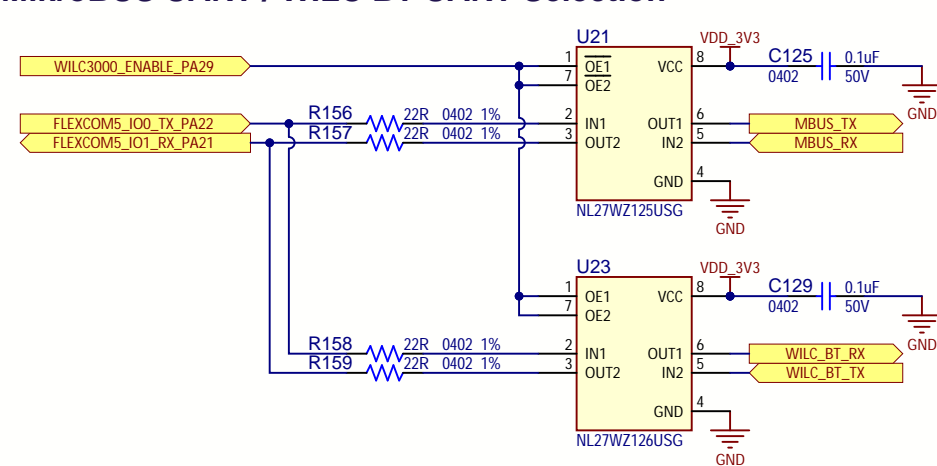
### CAN0 / DEBUG UART Selection



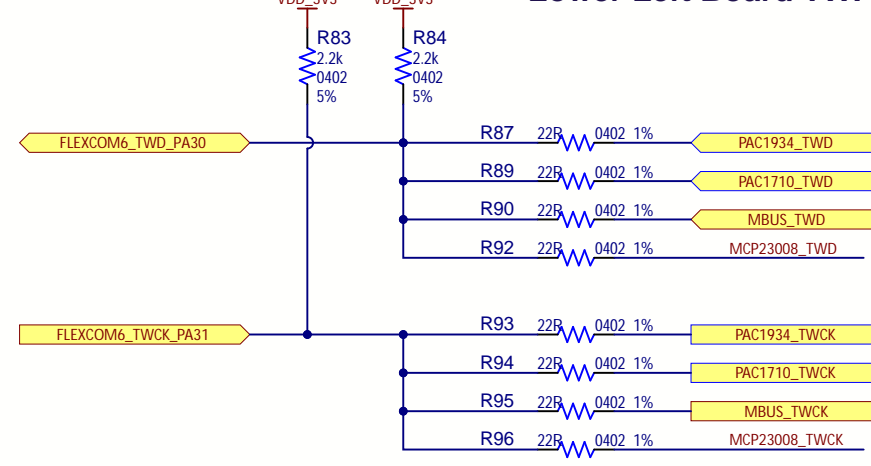
### Upper Right Board TWI





### MikroBUS UART / WILC BT UART Selection



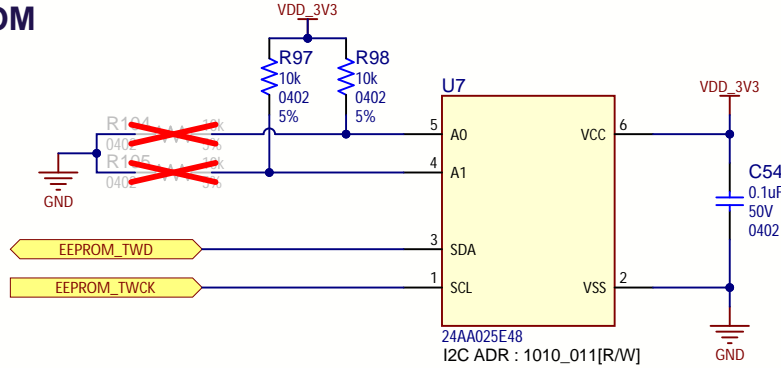
### Lower Left Board TWI



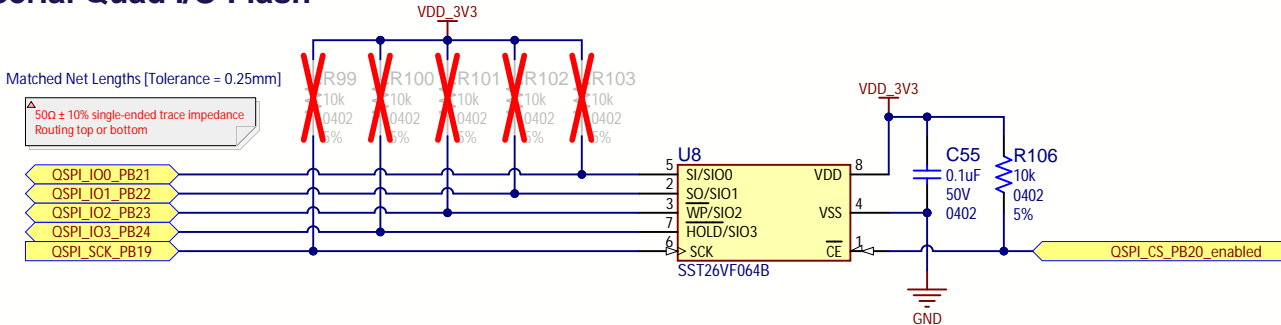
Drawn By: IGn		
Engineer: IGn		
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>	
Sheet Title Processor IO Expansions	Variant Name Standard_WILC3000_DNP	Designed with 
Size B	Sch #: 03-11039 Revision: 3	Date: 27.05.2022 10:17:20 Sheet 5 of 13
File: 05 - Processor IO Expansions.SchDoc		



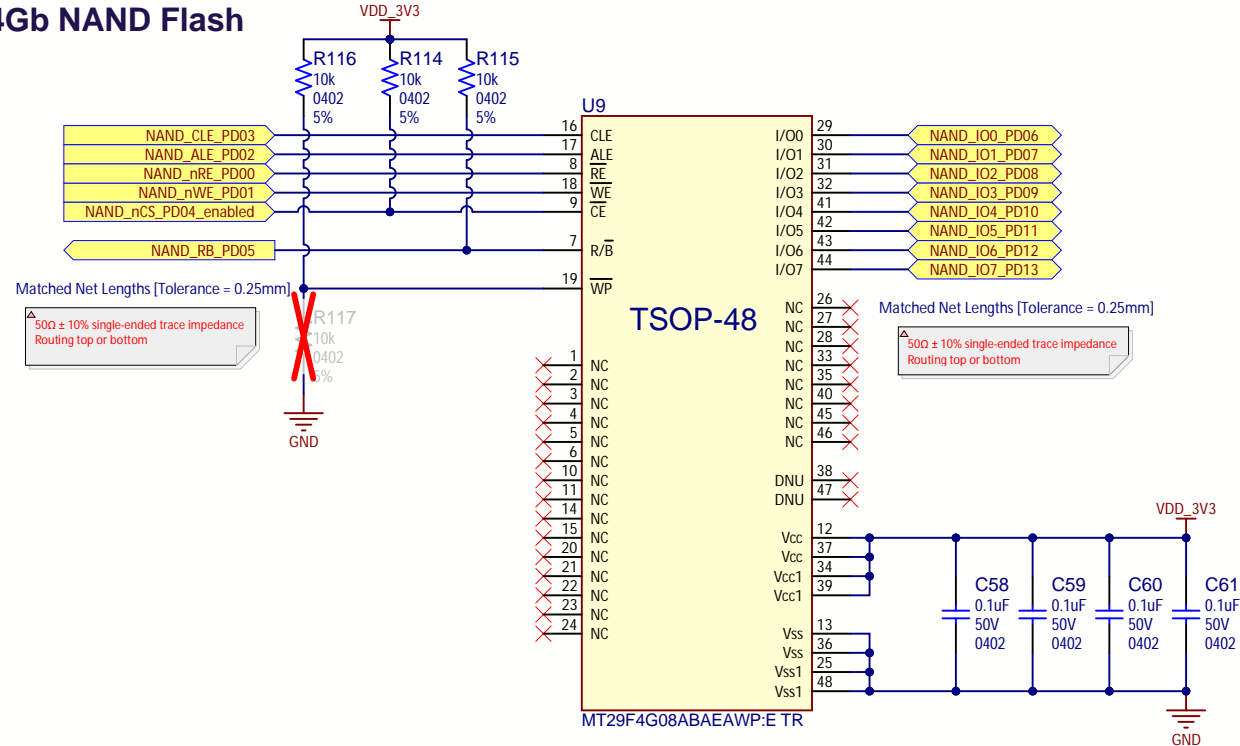
# TWI EEPROM



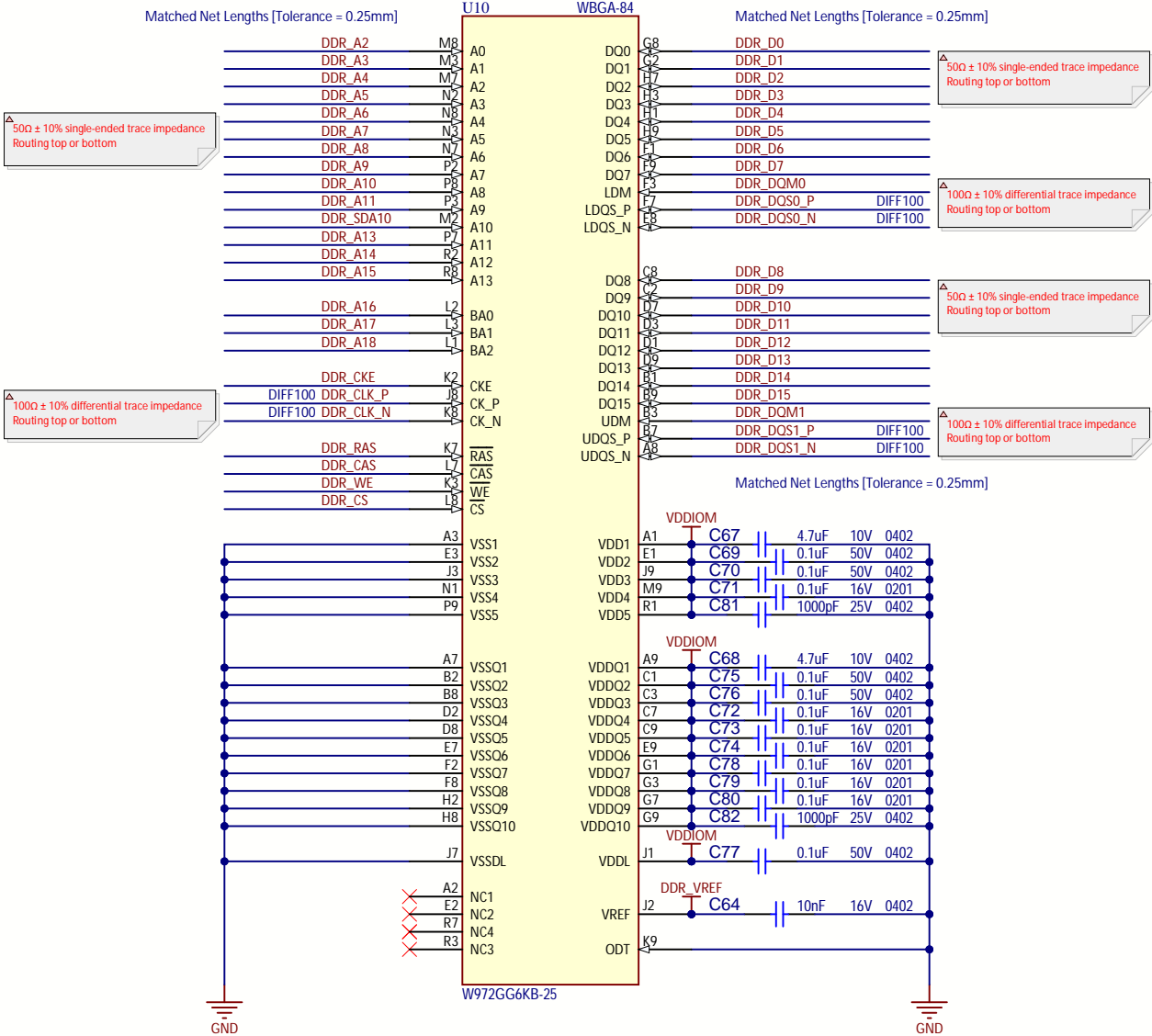
# Serial Quad I/O Flash



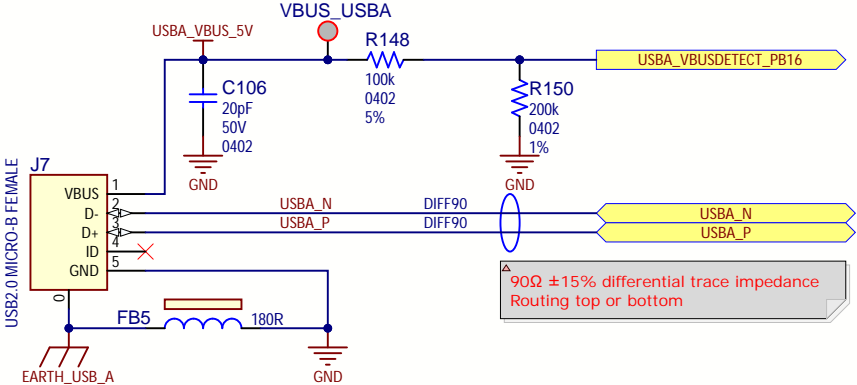
# 4Gb NAND Flash



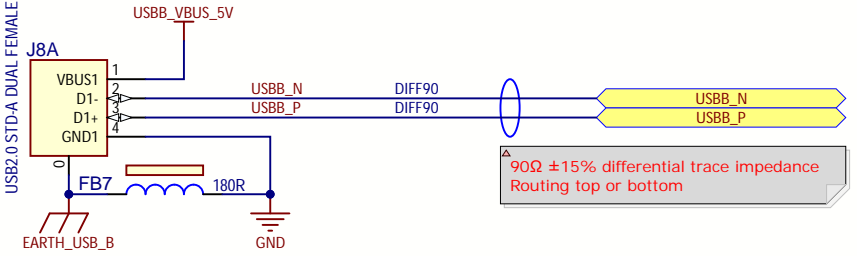
# 2Gb DDR2-800 SDRAM 16bit



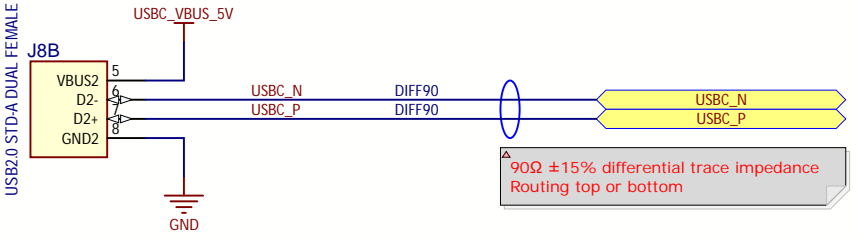
USB-A port



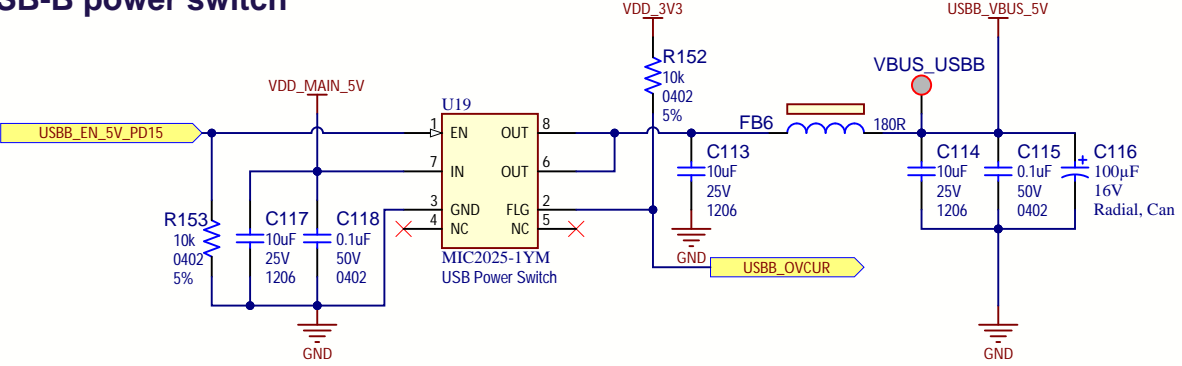
USB-B port



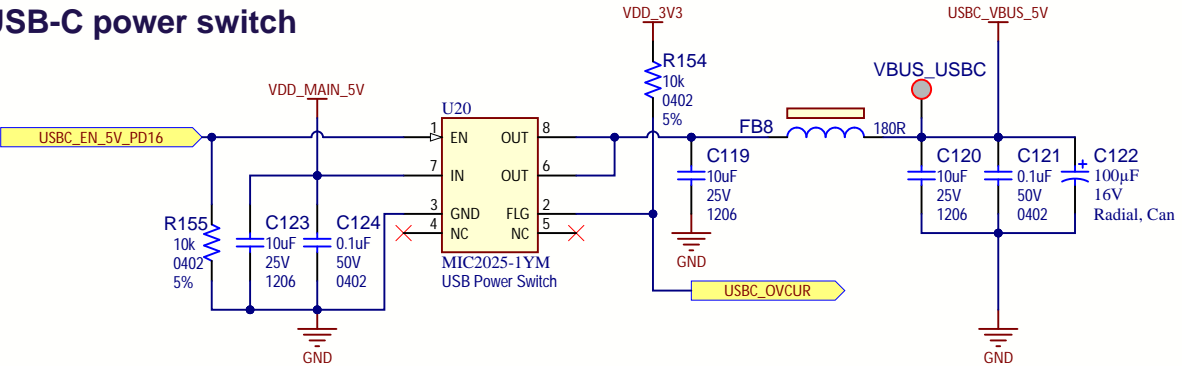
USB-C port

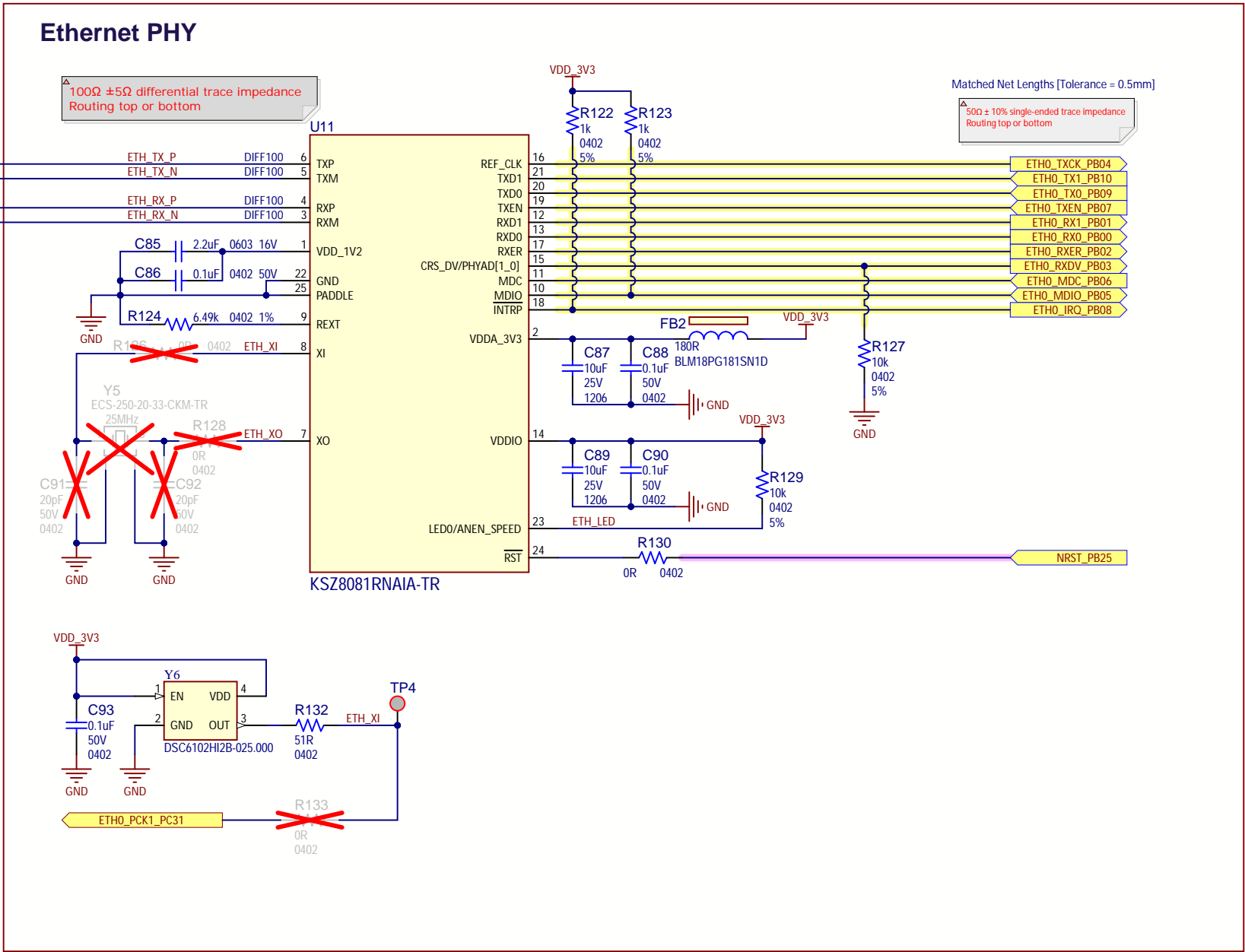
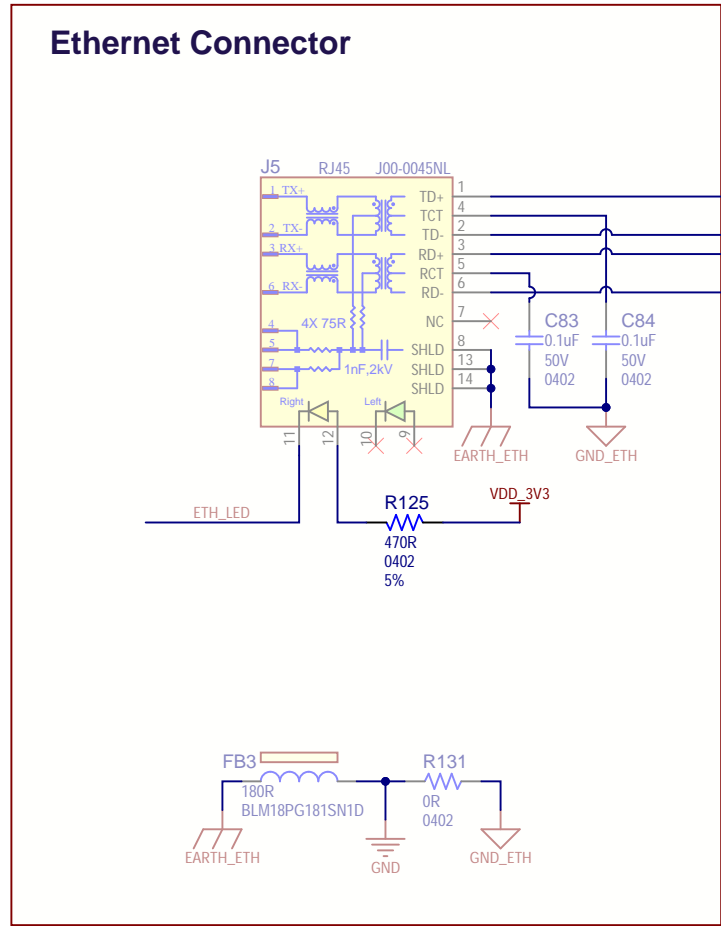


USB-B power switch



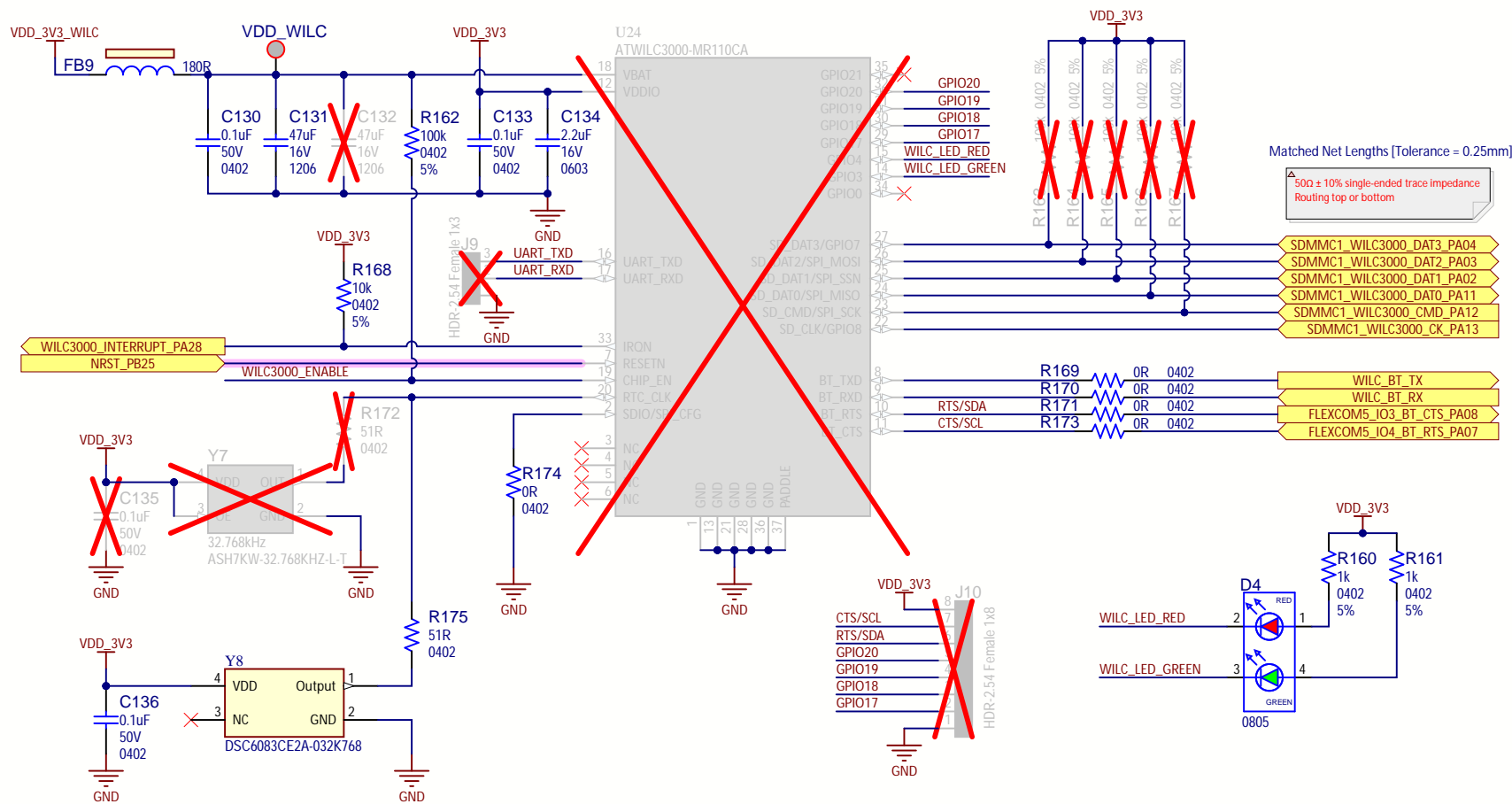
USB-C power switch



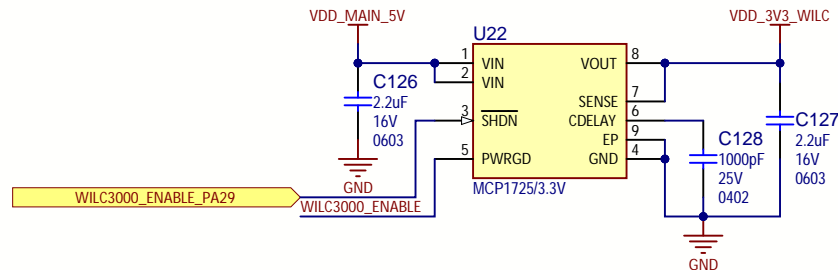






WiFi / BT MODULE

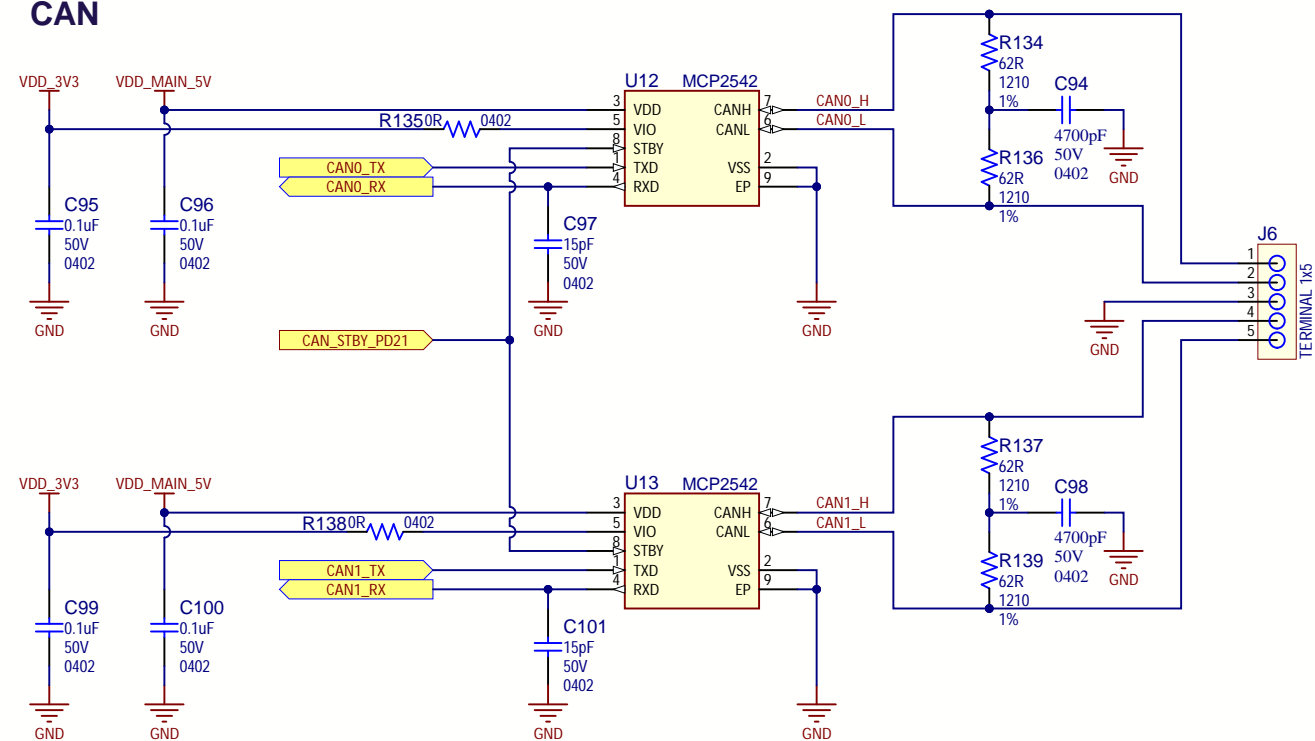


WiFi / BT ENABLE

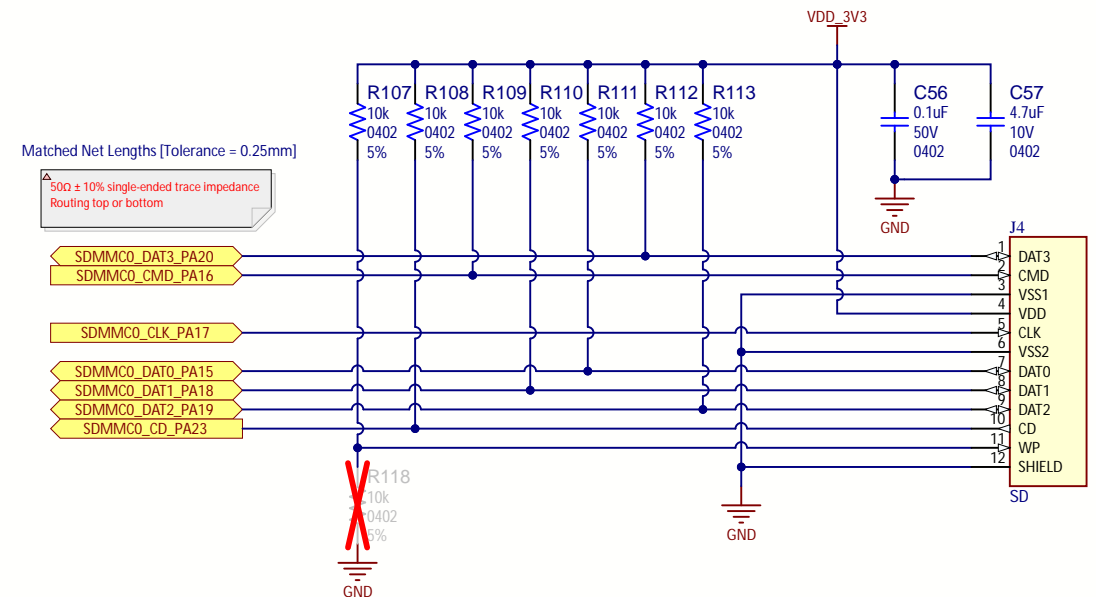


Drawn By: IGn			
Engineer: IGn			
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit		
Sheet Title Wi-Fi BT	Variant Name Standard_WILC3000_DNP	Designed with 	
Size B	Sch #: 03-11039	Date: 27.05.2022 10:17:21	
	Revision: 3	Sheet 9 of 13	
File: 09 - Wi-Fi BT module.SchDoc			

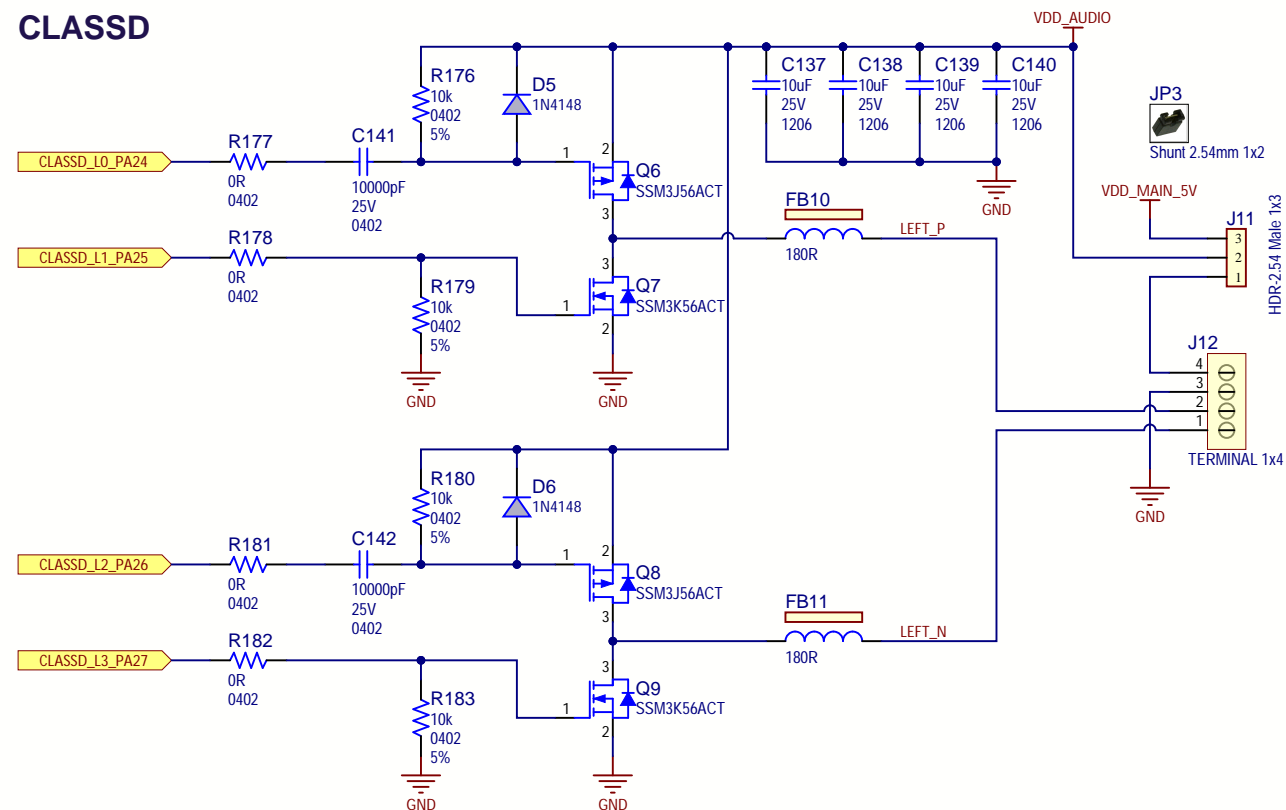
**CAN**





## SDMMC

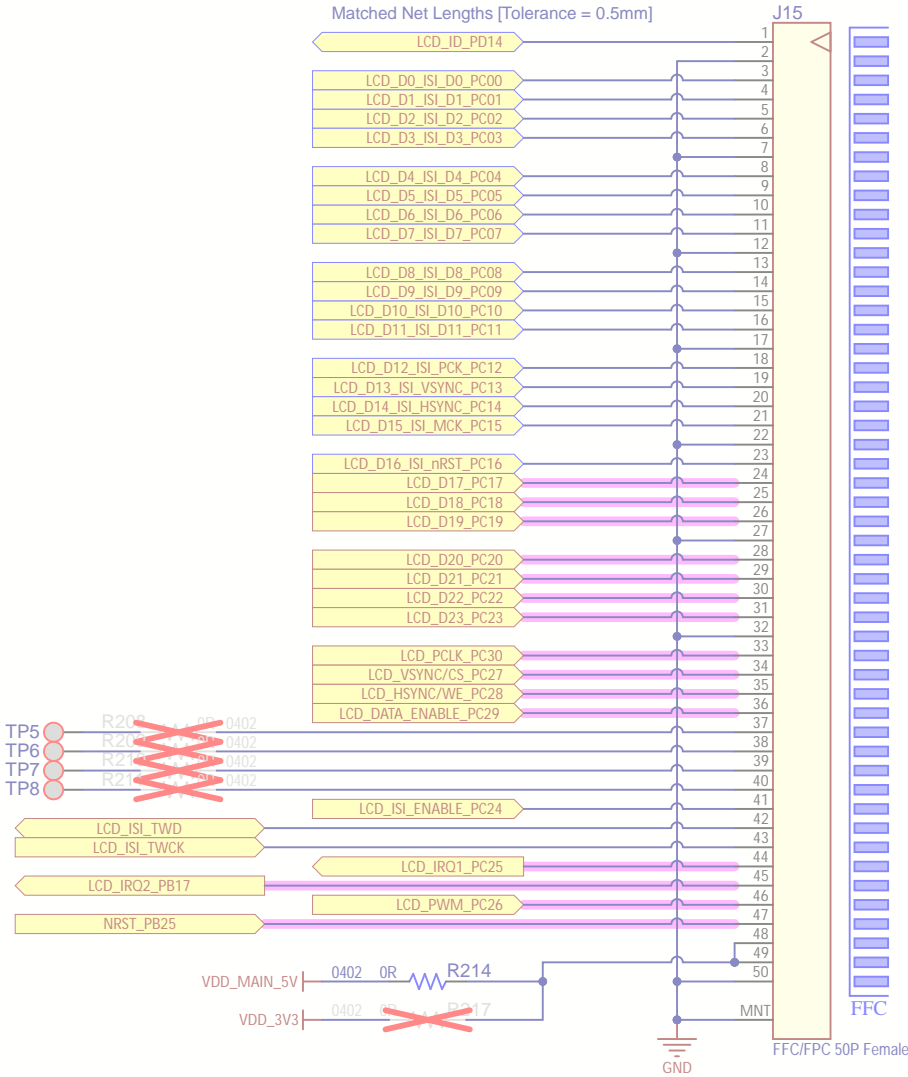


## CLASSSD

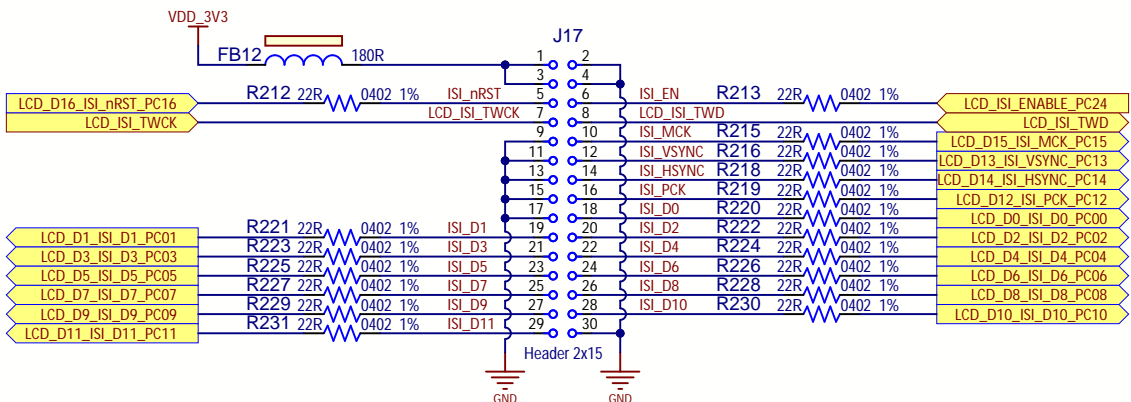


Drawn By: IGn		 <b>MICROCHIP</b>	
Engineer: IGn			
PartNumber: DT100126		Project Title <i>SAM9X60 Evaluation Kit</i>	
Sheet Title <b>SDMMC, CAN &amp; CLASSD</b>		Variant Name <b>Standard_WILC3000_DNP</b>	<i>Designed with</i>  <a href="http://Altium.com">Altium.com</a>
Size B	Sch #: 03-11039	Date: 27.05.2022 10:17:21	
	Revision: 3	Sheet 10 of 13	
File: 10 - SDMMC, CAN & CLASSD.SchDoc			

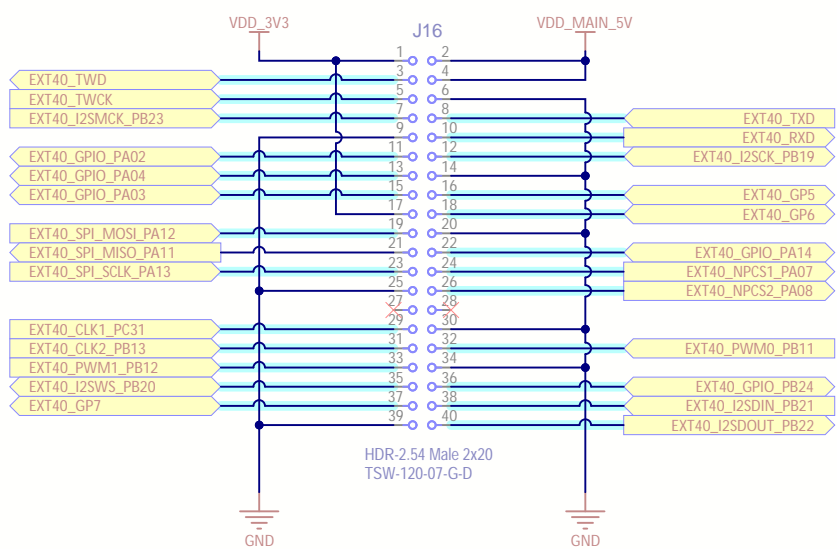
LCD



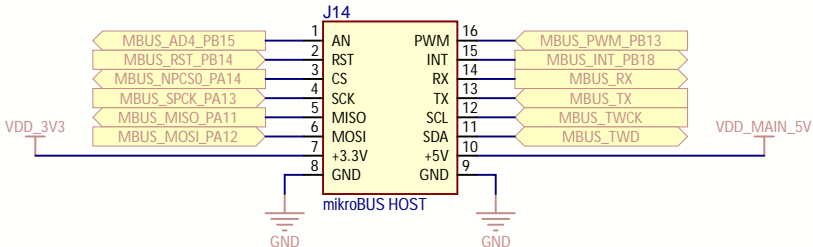
ISI Camera





40-pin GPIO

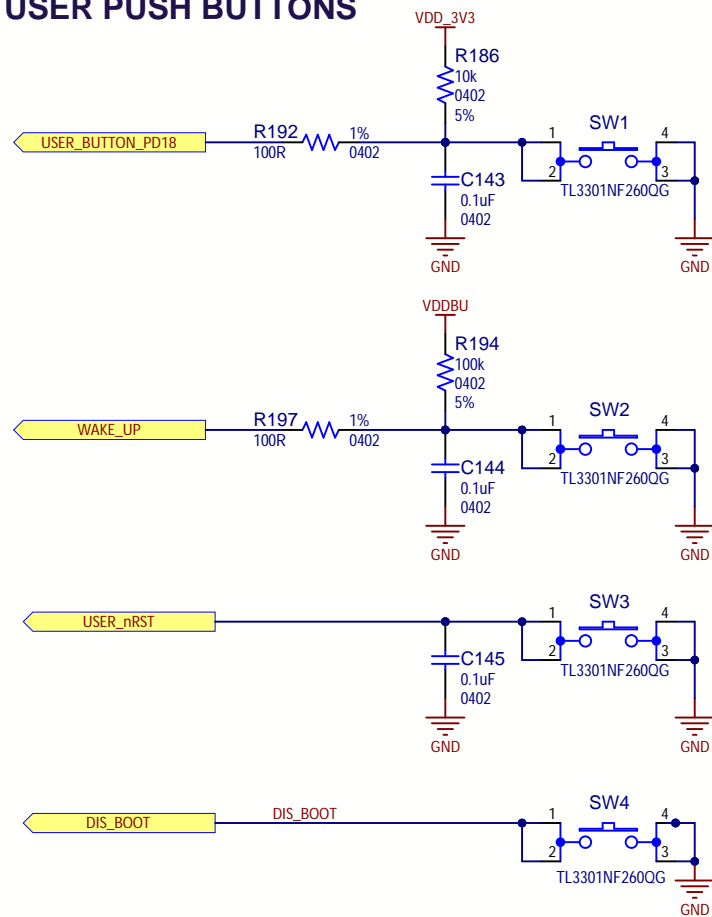


MikroBUS

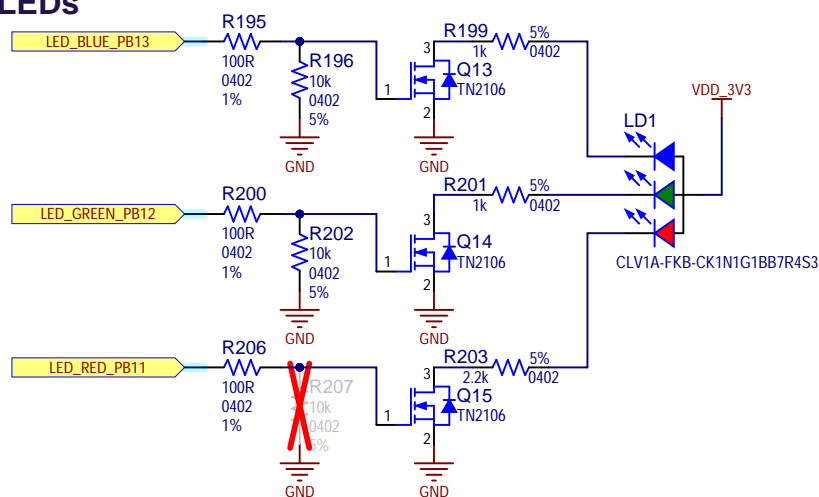


Drawn By: IGn	 <b>MICROCHIP</b>		
Engineer: IGn			
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>		
Sheet Title Expansion connectors	Variant Name Standard_WILC3000_DNP	Designed with  <b>Altium</b>	
Size B	Sch #: 03-11039	Date: 27.05.2022 10:17:21	Revision: 3
File: 11 - Expansion connectors.SchDoc		Sheet 11 of 13	Altium.com

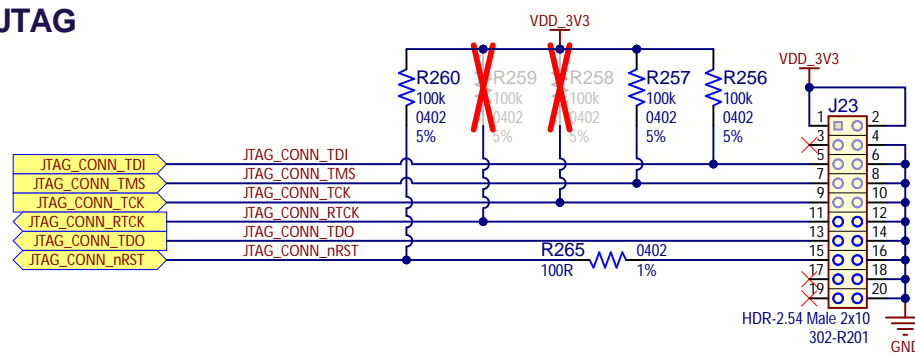
## USER PUSH BUTTONS



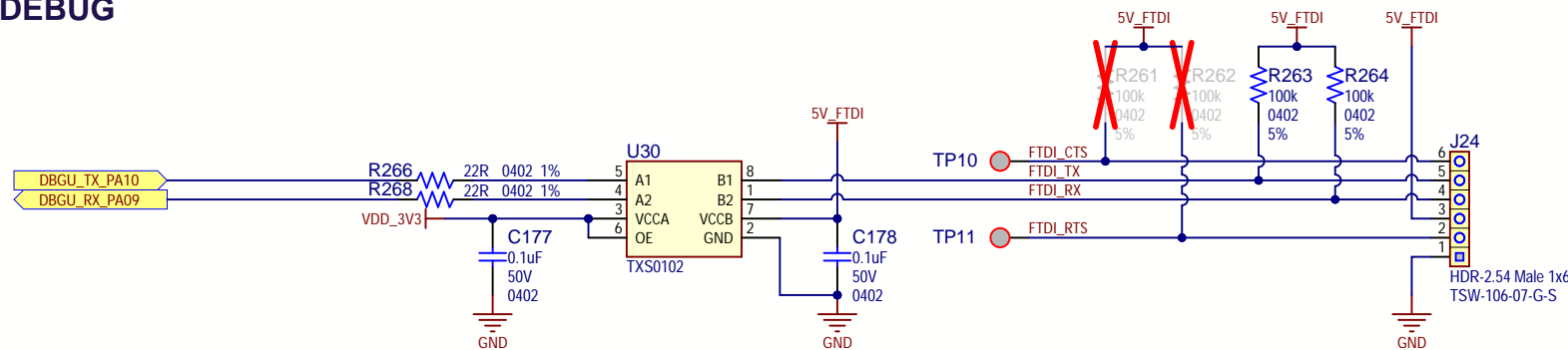
## RGB LEDs



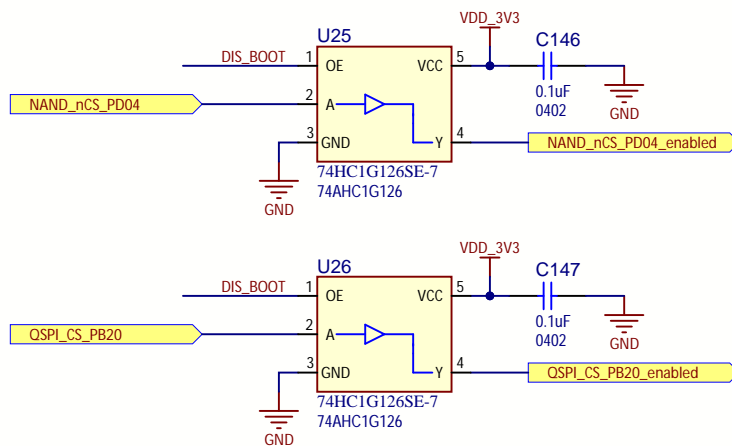
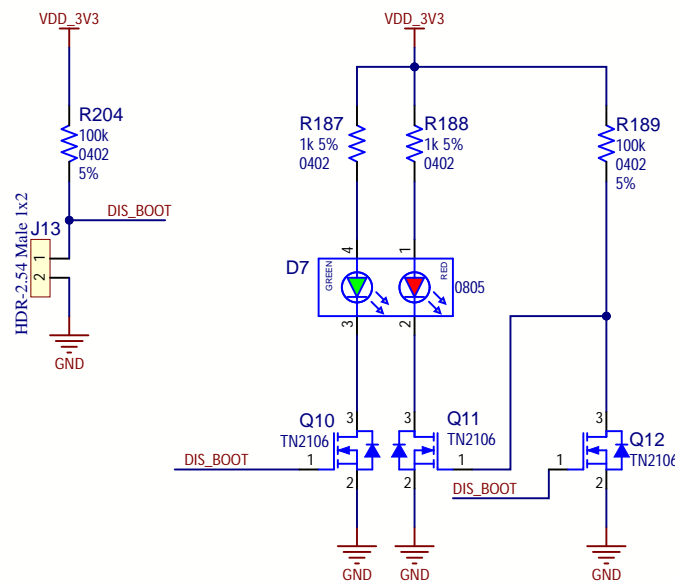
## JTAG



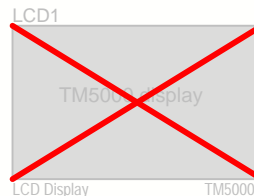
## FTDI DEBUG



## DISABLE BOOT



## PLACEHOLDERS



Drawn By: IGn		
Engineer: IGn		
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit	
Sheet Title User Interaction	Variant Name Standard_WILC3000_DNP	Designed with 
Size B	Sch #: 03-11039	Date: 27.05.2022 10:17:21
	Revision:3	Sheet 12 of 13
File: 12 - User Interaction.SchDoc		

