## Creating the First Application on PIC32CM JH01 Microcontrollers Using MPLAB Harmony v3 with MPLAB Code Configurator (MCC)



TB3353

## Introduction

MPLAB<sup>®</sup> Harmony v3 is a software development framework consisting of compatible and interoperable modules that include peripheral libraries (PLIBs), drivers, system services, middleware, and third-party libraries. The MPLAB Code Configurator (MCC) is a GUI-based tool that provides an easy way to enable and configure various MPLAB Harmony modules. The MCC is a plug-in to the MPLAB X Integrated Development Environment (IDE).

The PIC32CM JH family of microcontrollers (MCUs) is the next generation of the popular SAM C21 family of Arm<sup>®</sup> Cortex<sup>®</sup>-M0+ based MCUs. The PIC32CM JH family of MCUs delivers a variety of popular features plus extended memory options up to 512 KB of Flash and 64 KB to help create designs that need functional safety, enhanced touch, or security. This family pin is compatible with the SAM C21 family in the 32-pin, 48-pin and 64-pin packages.

This document explains how to create a simple application on a Cortex-M0+ based PIC32CM JH01 Microcontroller using the MCC with MPLAB Harmony v3 modules. The objective of this application is to toggle an LED on a timeout basis and print the LED toggling rate. For this demonstration, the following MPLAB Harmony v3 modules are used and configured using the MCC:

- The PORT pin to toggle LED.
- Real-Time Clock (RTC) PLIB to periodically sample LED toggling rate.
- External Interrupt Controller (EIC) PLIB to change the toggling rate when there is a switch press event.
- Serial Communication Interfaces (SERCOM (SERCOM configured as USART)) and DMA PLIBs to print LED toggling rate on COM port terminal application (Serial Console) running on a computer.

## 1. Creating the First Application on the PIC32CM JH01 MCU

The following software and hardware tools are used for this demonstration:

- MPLAB X IDE v6.20
- MPLAB Code Configurator Plug-in v5.5.0
- MPLAB XC32 Compiler v4.35
- MPLAB Harmony v3 repository: csp v3.18.2
- PIC32CM JH01 Curiosity Pro Evaluation Kit

**Note:** The latest versions of these tools can also be used to develop the application.

To create an MPLAB Harmony v3-based project, follow these steps:

- 1. On the **Start** menu, launch MPLAB X IDE.
- 2. On the **File** menu, click **New Project** or click the *New Project* icon.
- 3. In the New Project window, in the left Navigation bar, under Steps click **Choose Project**.

#### Figure 1-1. Choose Project

X New Project		×
Steps	Choose Project	
1. Choose Project 2	Q, Filter:	
	Categories: Microchip Embedded Other Embedded	Projects: Application Project(s) Prebuit (Hex, Loadable Image) Project User Makefile Project Library Project
	Description:	
	Creates a new application project. It uses	s an IDE-generated makefile to build your project.
	< Bade	Next > Finish Cancel Help

- 4. In the Choose Project property page:
  - a. Categories: Selelct Micorchip Embedded.
  - b. Project: Select Application Project(s).
- 5. Click Next.
- 6. In the left Navigation bar, under Steps click **Select Device**.



🔀 New Project		×
Steps	Select Device	
<ol> <li>Choose Project</li> <li>Select Device</li> <li>Select Plugin Board</li> <li>Select Compiler</li> <li>Select Compiler</li> <li>Select Project Name and Folder</li> <li>(Optional) Add Project</li> </ol>	Family:     All Families       Device:     PIC32CM25323H01100       Tool:     No Tool	
MPLAB X IDE		
< Back Add And	nother Project Next > Finish Cancel	Help

Figure 1-2. Selecting the Device

- 7. In the Select Device property page, in the **Device** box, type or select the device PIC32CM5164JH01100.
- 8. Click Next.
- 9. In the left Navigation bar, under Steps click **Select Compiler**.
- 10. In the Select Compiler property page, expand XC32 list of options, and then select **XC32 (v4.35)**.



#### Figure 1-3. Compiler Selection

🕱 New Project	×
Steps         1. Choose Project         2. Select Device         3. Select Header         4. Select Plugin Board         5. Select Compiler         6. Select Project Name and Folder         7. (Optional) Add Project	Select Compiler Compiler Toolchains C32 C32 (v4.35) [C:\Program Files\Microchip\xc32\v4.35\bin] XC32 (v4.30) [C:\Program Files\Microchip\xc32\v4.21\bin] XC32 (v4.21) [C:\Program Files\Microchip\xc32\v4.21\bin]
MPLAB X IDE	
< Back Add An	other Project Next > Finish Cancel Help

#### 11. Click Next.

- 12. In the left Navigation bar, click **Select Project Name and Folder**.
- 13. In the Select Project Name and Folder property page:
  - a. Project Name: Enter getting\_started\_pic32cm\_jh01\_cpro.
  - b. Project Location: Click the **Browse** button and choose C:\microchip\h3\Tech\_Brief.



#### Figure 1-4. Project Creation

🔀 New Project		×
Steps	Select Project Name a	nd Folder
<ol> <li>Choose Project</li> <li>Select Device</li> <li>Select Header</li> <li>Select Plugin Board</li> <li>Select Compiler</li> <li>Select Project Name and Folder</li> <li>(Optional) Add Project</li> </ol>	Project Name: Project Location: Project Folder: Overwrite existing p Also delete sources Set as main project Set as main project Use project location	getting_started_pic32cm_jh01_cpro C: \microchip \h3\Technical_Brief Browse \Technical_Brief\getting_started_pic32cm_jh01_cpro.X project. s. n Finish
MPLAB X IDE	Encoding: ISO-	8859-1
< Back Add And	other Project	Next > Finish Cancel Help

- 14. Click Finish.
- 15. Before launching the MCC, the Configuration Database Setup window will be displayed, where the Device Family Pack (DFP) and Cortex<sup>®</sup> Microcontroller Software Interface Standard (CMSIS) path can be changed if required. For this demonstration, the default settings are used.
- 16. The MCC plug-in will open in a new window as shown in the following figure.

Figure 1-5. MPLAB Code Configurator Window

Edit View Navigate Source Refactor Production Debu			Q Search (Ctrl+I)
) 🥙 🎦 🔛 🍓 🔤 default 🛛 🦻	🗸 🌬 - 🎼 - 😼 - 🕨 - 👱 - 👱	- 🏹 🌆 - 🞯 - 🚥 🧶 🛒 - 🔜 PC:	0x0 How do I? Keyword(s)
Resource Management [MCC] × •	Kit Window 🗴 Start Page 🗴 🐏 main.c	x 🖭 initialization.c x Project Graph x	
MCC v5.3.7	🗙 🗔 📖 🖲 📖 📾 🛧 🏟 Plugins	· ~	View: Root 🗸
Project Res Gen Im E 😮 🚇			
► Libraries	NVMCTRL		
▼ System	Peripheral Library		Configuration
🛞 🔵 System	MEMORY 🔶		Options
		Project	options
	EVSYS Peripheral Library	Graph	
Project Resources	Perpheral Cibrary	Старн	
	Device Family Pack (DFP)		
	System		
Device Resources	CMSIS Pack		
Libraries	CHOID F JUK		
Harmony			

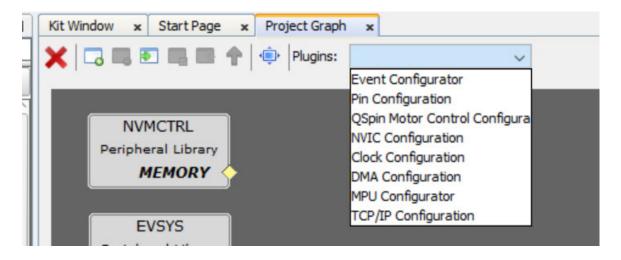
### 1.1 Adding and Configuring the MPLAB Harmony Components

To add and configure MPLAB Harmony components using the MCC, follow these steps:

1. In the MCC window, click **Project Graph**.



Figure 1-6. MPLAB Code Configurator



In the **Plugins** drop-down list, select **Clock Configuration**. The **Clock Easy View** window will be displayed, verify that the Main Clock is set to 48 MHz.
 **Note:** Make sure to make the following modification for the GCLK Generator 1.

S Clock Easy View - Editor ٥ Clock Easy View × Oscillators Controller xosc GCLK Generator 0 Main Clock Internal 🗹 OSC48M OSC48M -CPU 48,000,000 Hz FDPLL CFD 48,000,000 Hz OSCULP32K VDD = 3.6V to 5.5V 🗸 48 MHz ~ DIV1 ~ GCLKO ge Calibration OSC32K 48,000,000 Hz хозсак xosc . GCLK\_IN[0] 1÷ eripheral Clock Selectio GCLK1 GCLKO CRYSTA GCLK1 ~ GCLK2 Fractional Digital Phase Locked Loop GCLK3 GCLK Generator 1 GCLK4 Peripherals XOSC FDPLL GCLK5 £63 OSC48M GCLK6 Peripheral Clock Configuration FDPLL GCLK7 0 🗘 0 🗘 DIV1 V OSCULP32K GCLK1 GCLK8 OSC32K Auto Calculate ~ хозсак EIC £63 1 🗘 32,768 Hz GCLK\_IN[1] WDT 1,024 Hz 32 KHz Os OSCULP1K OSC32K 32 KH OSCULP32K OSC1K OSC1K GCLK Generator x (x=2-8) GCLK 2 RTC OSC32K 1 024 H

Figure 1-7. MPLAB Code Configurator - GCLK Generator One

- 3. Under **Device Resources**, expand the list of options *Harmony > Peripherals > SERCOM*.
- 4. Click **SERCOM4** and observe that the SERCOM4 Peripheral Library block is added in the Project Graph window.



Figure 1-8. MPLAB Code Configurator - Selection of Peripherals

Resource Management [ MCC v5.5.0	[MCC]				× •	
Project Resources	Generate	Import	Export	•		
Device Resources						
<ul> <li>Peripherals</li> </ul>					^	
► AC						
ADC						
CAN						
► CCL						
► DAC						
DIVAS						
DSU						
FREQM						
PDEC						
► PM						
RAM						
RSTC						
▼ SERCOM						
🛞 🛨 🔿 s	SERCOM0					
🛞 🛨 🔿 s	SERCOM1					
🛞 🛨 🔿 s	SERCOM2					
🕜 🕂 🔿 s	SERCOM3					
8 🛨 🕓 s	SERCOM4					
⊕ + ○ ≤	SERCOM5					

**Note:** Users can also select other peripherals under Device Resources, Harmony > Peripherals.

5. In the Project Graph window, in the left Navigation bar, select **SERCOM4 Peripheral Library**. In the right **Configuration Options** property page, configure it as follows to print the data on the Serial Console at 115200 baud rate.



c pin_configurations.csv x Proconfiguration Options x of	•
NVMCTRL   Peripheral Library   MEMORY	

#### Figure 1-9. MPLAB Code Configurator - SERCOM4 Configuration

- 6. Under **Device Resources** expand the list of options *Harmony* > *Peripherals* > *EIC*. Click **EIC** and observe that the EIC Peripheral Library block is added in the Project Graph window.
- 7. In the right Configuration Options property page, select the **Enable Interrupt** check box and the **Enable EIC Channel3** check box for the switch press event.

Figure 1-10. MPLAB Code Configurator - EIC Configuration

Kit Window 🗴 Start Page 🗴 Project Graph 🗴	Configuration Options ×						
🗙 🗔 🛤 🖲 📾 🛖 🏟 Plugins:	- +						
	B-EIC						
NVMCTRL	EIC Clock Source Selection Clocked by ULP32K V						
Peripheral Library	Non Maskable Interrupt Control 🗌						
	Enable EIC Channel0						
	Enable EIC Channel1						
EVSYS	Enable EIC Channel2						
Peripheral Library Peripheral Library	🖻 <mark>Enable EIC Channel3 🛛 🗹</mark>						
	🖻 EIC Channel3 Configuration						
Device Family Pack (DFP)	Enable Interrupt						
	Enable Event Output						
System	-External Interrupt3 Detection Clock Edge detection is dock synchronously operated $$						
CMSIS Pack	External Interrupt3 Edge Selection $\sim$ Rising edge detection $\sim$						
CMSIS Pack	Enable Debounce						
SERCOM4	Enable filter						
Peripheral Library	Enable EIC Channel4						
12C 🔶	Enable EIC Channel5						
SPI 🔷	-Enable EIC Channel6						
UART	-Enable EIC Channel7						
	-Enable EIC Channel8						

8. Under **Device Resources** expand the list of options *Harmony > Peripherals > RTC*. Click **RTC** and observe that the RTC Peripheral Library block is added in the Project Graph window to generate a compare interrupt every 500 milliseconds. Slect the **Compate 0 Interrupt Enable** check box.



**Note:** The Compare Value is set as 0x200. This compare value generates an RTC compare interrupt every 500 milliseconds. RTC clock = 1024 Hz RTC Prescaler = 1 Required Interrupt rate = 500 ms

Hence, Compare Value = (500/1000) x 1024 = 512 (i.e., 0x200)

#### Figure 1-11. MPLAB Code Configurator - RTC PLIB Configuration

Projects × Files	Kit Window x Start Page x Project Graph x	Configuration Options	×
pic32cm_jh01_cpro_getting_started	🗙 🗔 🛤 💽 🖏 📾 🔶 Plugins:		
Device actions     Header Files		⊟-RTC	
Important Files	NVMCTRL	-Hardware Settings	
🗈 🛅 Linker Files	NVMCTRL Peripheral Library	-Generate Frequency Correction API	
⊕-/@ Source Files ⊕-/@ Libraries	MEMORY	-RTC Count Sync Enable	
⊕- 🙀 Loadables		E-RTC Operation Mode 32-bit Counter with Single 32-bit Compare 🗸	
		EIC B-RTC MODE 0 Configuration	
	Peripheral Library Periphera	Inal Library	
	Device Family Pack (DFP)	- Periodic Interval O Interrupt Enable	
		Periodic Interval 1 Interrupt Enable     Periodic Interval 2 Interrupt Enable	
	System	RTC — Periodic Interval 2 Interrupt Enable	
		TMR Periodic Interval 4 Interrupt Enable	
	CMSIS Pack	- Periodic Interval 5 Interrupt Enable	
	SERCOM4	-Periodic Interval 6 Interrupt Enable 🗌	
	Peripheral Library	- Periodic Interval 7 Interrupt Enable	
	12C 🔶	– Compare O Interrupt Enable 🛛	
	SPI 🔶	Overflow Interrupt Enable	
	UART 🔶	RTC Prescaler DIV1 V	
		Compare Value 0x 200 🜩	
		Clear on compare Match 🗹	

9. From the Plugins drop-down list, select **Add Channel** and then select **DMA Configuration**. Configure the DMA Channel 0 to transmit application buffer to the USART TX register. The DMA transfers one byte from the user buffer to the USART transmit buffer on each trigger.

Figure 1-12	. MPLAB Code	Configurator -	DMA Configuration
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Configuration				-	٥
Active	Channels List	Use Linked List Mode	ę		
Channel Number	Trigger	]	DMA Channel 0 Settings		
DMAC Channel 0	SERCOM4_Transmit 🗸	Enable Interrupt			
		Trigger Action	One Beat Transfer per DMA Request		~
Add Channel	Remove Selected Channel	Source Address Mode	Increment Address After Every Transfer		~
		Destination Address Mode	Fixed Address Mode		~
		Beat Size	8-bit bus transfer		~

- 10. From the Plugins drop-down list select **Pin Configuration** and then click **Pin Settings**.
- 11. In the **Order** box, type or select Ports. Build configurations according to the application as indicated below. Change the Custom Name of the pin IDs, PC05 and PB19, as shown in the following figure.



Kit Window 🗙	Start Page ×	Packs x Project Gra	ph 🗴 Pin Diagram	× Pin Tabl	e 🗴 Pin Settings	×			
Order: Ports	~	Table View 🗹 Easy	View						
Pin Number	Pin ID	Custom Name	Function	Mode	Direction	Latch	Pull Up	Pull Down	Drive Strength
32	PB10		SERCOM4_PAD2 ~	Digital	High Impedance $\lor$	n/a			NORMAL V
33	PB11		SERCOM4_PAD3 ~	Digital	High Impedance $\lor$	n/a			NORMAL V
67	PB19	SWO	EIC_EXTINT3 ~	Digital	High Impedance $$	n/a			STRONG 🗸
21	PC05	LED0	GPIO 🗸	Digital	Out 🗸	Low			NORMAL 🗸

Figure 1-13. Pin Settings Window - SERCOM, EIC and LED Pin Configuration

### **1.2** Generating the Code

After configuring the peripherals, as shown in the following figure click **Generate** under **Resource Management** [MCC].

Figure 1-14. Code Generation

WPLAB X IDE v6.20 - pic32cm\_jh01\_cpro\_getting\_started : pic32cm\_jh01\_cpro

File	Edit View Navigate Sour	ce Refactor	Production	Debug	Team	Tools	1
P	) 🛅 🔛 🖣 🦻	C pic32	cm_jh01_cpro	~	SRC -		÷
or D	Resource Management [MCC] MCC v5.5.0						
Navigator	Project Resources	Generate	Import	Export		0	
ard 📎 I	<ul> <li>Libraries</li> <li>Harmony</li> </ul>						

#### Notes:

- The generated code will add files and folders to the 32-bit MCC Harmony project. In the generated code, notice the Peripheral Library files generated for the Real-Time Clock (RTC), External Interrupt Controller (EIC), PORT peripherals, SERCOM4 (as Universal Synchronous Asynchronous Receiver Transmitter (USART)), and the Direct Memory Access (DMA) peripherals. The MCC also generates the main.c file.
- 2. The MCC provides an option to change the generated file name. By default, the file name main.c is generated if a name is not assigned.

### 1.3 Adding Application Logic to the Project

 To develop and run the Application, use the following steps: Open the main.c file of the project and add the following application logic. Add the following code in the main() function:

```
uint8_t uartLocalTxBuffer[100] = {0};
    /* Initialize all modules */
    DMAC_ChannelCallbackRegister(DMAC_CHANNEL_0, uartDmaChannelHandler_Tx, 0);
    RTC_Timer32CallbackRegister(rtcEventHandler, 0);
    EIC_CallbackRegister(EIC_PIN_3, SW0_userHandler, 0);
    sprintf((char*)uartTxBuffer, "*********** Printing Toggling LED rate
    **********\r\n");
    DMAC_ChannelTransfer(DMAC_CHANNEL_0, uartTxBuffer, \
        (const void *)&(SERCOM4_REGS->USART_INT.SERCOM_DATA), \
        strlen((const char*)uartTxBuffer));
```



```
/* Start the timer */
RTC_Timer32Start();
```

Figure 1-15. Adding Application Logic to Register Callback Event Handlers



2. Implement the registered callback event handlers for the peripherals by adding the following code before the main() function.

```
static void SW0_userHandler(uintptr_t context)
{
    changeTempSamplingRate = true;
}
static void rtcEventHandler (RTC_TIMER32_INT_MASK intCause, uintptr_t context)
{
    if (intCause & RTC_MODE0_INTENSET_CMP0_Msk)
    {
        isRTCExpired = true;
    }
}
static void uartDmaChannelHandler_Tx(DMAC_TRANSFER_EVENT event, uintptr_t contextHandle)
{
    if (event == DMAC_TRANSFER_EVENT_COMPLETE)
    {
        isUARTTxComplete = true;
    }
}
```

3. According to the status of the isRTCExpired and isUARTTxComplete flags (These flags are handled by rtcEventHandler and uartDmaChannelHandler\_Tx event handlers when RTC Timer expires and when UART completes the transfer of data.), the LEDO is toggled at a default rate of 500 ms. To change the toggling rate, if the user presses the SWO switch, the toggling rate changes to 1s, 2s, and 4s and back to 500 ms with subsequent switch press events. SWO\_userHandler will be responsible for changing the toggling rate when the user presses the SWO switch on the board.



Inside the while loop, delete  $SYS_Tasks()$  and add the following code to toggle the LED at a default rate of 500 ms.

4. Add the following code immediately after adding the above code to change the toggling rate when there is a switch press event.

```
if(changeTempSamplingRate == true)
            {
                changeTempSamplingRate = false;
                if (tempSampleRate == TEMP SAMPLING RATE 500MS)
                    tempSampleRate = TEMP SAMPLING RATE 1S;
                    RTC Timer32CompareSet(PERIOD_1S);
                else if(tempSampleRate == TEMP SAMPLING RATE 1S)
                    tempSampleRate = TEMP SAMPLING RATE 2S;
                    RTC_Timer32CompareSet(PERIOD_2S);
                else if (tempSampleRate == TEMP SAMPLING RATE 2S)
                    tempSampleRate = TEMP SAMPLING_RATE_4S;
                    RTC Timer32CompareSet(PERIOD_4S);
                else if(tempSampleRate == TEMP SAMPLING RATE 4S)
                   tempSampleRate = TEMP SAMPLING RATE 500MS;
                   RTC Timer32CompareSet(PERIOD 500MS);
                else
                {
                    ;
                RTC Timer32CounterSet(0);
                sprintf((char*)uartLocalTxBuffer, "LED Toggling rate is changed to
%s\r\n", &timeouts[(uint8_t)tempSampleRate][0]);
                DMAC ChannelTransfer(DMAC CHANNEL 0, uartLocalTxBuffer, \
                     \overline{(\text{const void }^*)} (SERCOM4 REGS->USART INT.SERCOM DATA), \
                    strlen((const char*)uartLocalTxBuffer));
            }
```

5. Add the following code to include the necessary header files, and define the macros for different RTC compare values.

#define TX\_BUFFER\_SIZE 100

This code declares the various flags whose status is monitored and changed by the event handlers in the application. It has various declarations and definitions of arrays used to print the LED toggling rate on the console.

static volatile bool isRTCExpired = false; static volatile bool changeTempSamplingRate = false; static volatile bool isUARTTxComplete = true;



```
static volatile bool isUARTRxComplete = false;
static uint8_t uartTxBuffer[TX_BUFFER_SIZE] = {0};
typedef enum
{
    TEMP_SAMPLING_RATE_500MS = 0,
    TEMP_SAMPLING_RATE_1S = 1,
    TEMP_SAMPLING_RATE_2S = 2,
    TEMP_SAMPLING_RATE_4S = 3,
} TEMP_SAMPLING_RATE_4S = 3,
} TEMP_SAMPLING_RATE;
static TEMP_SAMPLING_RATE tempSampleRate = TEMP_SAMPLING_RATE_500MS;
static const char timeouts[4][20] = {"500 milliSeconds", "1 Second", "2 Seconds", "4
```

### 1.4 Building and Programming the Application

- 1. The PIC32CM JH01 Curiosity Pro Evaluation Kit supports debugging using an Embedded Debugger (EDBG). Connect the Type-A male to micro-B USB cable to the micro-B debug USB port on the PIC32CM JH01 Curiosity Pro Evaluation Kit to power and debug the PIC32CM JH01 Curiosity Pro Evaluation Kit.
- 2. Ensure that the compiler optimization is set to 1, to check that follow these steps:
  - a. Right-click on the project *getting\_started\_pic32cm\_jh01\_cpro*, a shortcut menu appears. Click **Properties.**

e Edit View Navigate Source Refactor	r Production Debug Team Tools V	/indow Help Q Search (Ctrl+I)
🤊 🥙 🎦 🎦 😼 🔤	ault 🗸 🕅 🗸 🖓 🗸	🌇 + 🜔 + 🖳 + 🏪 + 🎧 🖏 + PC: 0x0 How do 12 (Zeyword(s) 👘 🌇 🦉 🔟 + 🔜 + 🛒 + 🕮 🚥
rojects × Files		Page x 🛒 MPLAB X Store x 🐻 Output x 🥪 Variables x 🗐 Call Stack x 🖬 Breakpoints x 🖷 main.c x 🖷 initialization.c 🚺 🛡
getting_started_pic32cm_jh01	Build	_pic32ck_sg01_cult × EDBG × EDBG-getting_started_pic32cm_jh01_cpro × getting_started_pic32cm_jh01_cpro (Build, Load,) ×
Device actions     Header Files	Clean and Build	
Important Files	Clean	
🕀 🛅 Linker Files	Batch Build	
Plugins	Set Configuration	
Gource Files     Joraries	ser comgaration	
Eloranes	New	
	Add	lons:
	Unset as Main Project	
	Find	val
	Locate Headers	164JH01100 found.
	-	0x1
	Run	
	Debug	lges for operation
	Step into	
	Make and Program Device	rea(s) will be programmed:
	Projects	rea(s) will be programmed: address = 0x0, end address = 0x1dff
avigator × ed_pic32cm_jh01_c	Close	
PERIOD_1S	Open Required Projects	, y ranges on this device, only the areas of memory that have been loaded with code (via the build process or loadin
PERIOD_2S	Code Assistance	read by default. If you wish to read custom ranges, please go to the Memories to Program property page and specify
PERIOD_4S		o read.
PERIOD_500MS     TEMP_SAMPLING_RATE	Analysis	
TX_BUFFER_SIZE	Show Code Coverage Summary	
whangeTempSamplingRate	Versioning	
isRTCExpired	History	
	Properties	

#### Figure 1-16. Project Properties

- b. In the **Project Properties** window, for Option categores select **Optimization** and for the Optimation level from the item list select **1**.
- c. Click **OK** to close the **Project Properties** window.



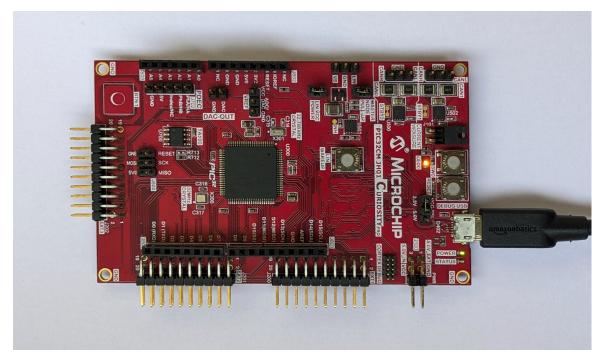
X

#### Figure 1-17. Optimization level

🔯 Project Properties - getting\_started\_pic32cm\_jh01\_cpro

Categories: Categories: Categories: File Inclusion/Exclusion File Inclusion/Exclusion Conf: [default] Conf: [default] Categories: Conf: [default] Conf: [default] Categories: Conf: [default] Categories: Conf: [default] Categories: Conf: [default] Conf: [default]	Options for xc32-gcc (v4.35) Option categories: Optimization optimization-level Unroll loops Omit frame pointer Pre-optimization instruction scheduling Post-optimization instruction scheduling	Reset				
<ul> <li> <ul> <li></li></ul></li></ul>	Use common tentative definitions Disable  Additional options:  Option Description Generated Command Line					
Manage Configurations Manage Network Tools	<ul> <li>O - Do not optimize. The compiler's goal is to reduce the cost of compilation and to make debugging produce the expected results.</li> <li>1 - Optimize. Optimizing compilation takes somewhat longer, and a lot more host memory for a large function. The compiler tries to reduce code size and execution time.</li> <li>2 - Optimize even more. The compiler performs nearly all supported optimizations that do not involve a space-speed trade-off.</li> <li>2 - Optimize uset more function speed (superset of O2).</li> </ul>					
Manage Network Tools		K Cancel Apply Unlock Help				

Figure 1-18. Hardware Setup



3. Set getting\_started\_pic32cm\_jh01\_cpro as the main project, and from Project Properties,
 select the latest compiler version (v4.35). Clean and build your project by clicking the highlighted
 icon.





4. Program the application by clicking the highlighted icon.

Figure 1-20. Program the Device



### **1.5** Observing the Output on the Board and Serial Terminal

- 1. Press the **Start** button to open Tera Term or PuTTY terminal application on the computer from the **Start** menu.
- 2. Select the required Serial Port and then click **OK**.

Figure 1-21. Selection of Serial COM Port

Tera Term: New connection	nc		×
O TCP/IP	Host: myhost.exa	mple.com	7
	✓ History		
	Service: $\bigcirc$ Telnet	TCP port#: 22	
	⊚ S\$H	SSH version: SSH2 ~~	
	○ Other	IP version: AUTO	~
● Serial	Port: COM27: ED	BG Virtual COM Port (COM2	~
	0K Cancel	Help	

3. In the **Tera Term Serial port setup and connection** dialog box, in the **Speed** box type or select the baud rate to **115200**.



#### Figure 1-22. Setting the Baud Rate

 $\times$ Tera Term: Serial port setup and connection COM27 Port: New setting 115200 Speed: 110 Data: 300 Cancel 600 Parity: 1200 2400 Help Stop bits: 4800 9600 Flow control: 14400 19200 Transmit 38400 57600 0 b msec/line 115200 230400 460800 Device Friendly Nam 921600 COM Port (COM27) Device Instance ID: USDIVIU\_USED&PID\_2111&MI\_01\6&189E889; Device Manufacturer: Microchip Technology, Inc. Provider Name: Microchip Technology, Inc. Driver Date: 10-15-2020 Driver Version: 7.5.0.0 < >

- 4. An LED (LED0) on the PIC32CM JH01 Curiosity Pro Development Evaluation Kit toggles on a timeout basis and the default periodicity of the timeout is 500 milliseconds.
- 5. The LED toggling rate is displayed on the Serial Terminal.
- 6. Press the switch SW0 on the PIC32CM JH01 Curiosity Pro Evaluation Kit to change the default periodicity of the timeout to one second.
- 7. Every subsequent pressing of the switch SW0 on the PIC32CM JH01 Curiosity Pro Evaluation Kit changes the periodicity of the timeout to 2 seconds, 4 seconds, 500 milliseconds, and back to 1 second in cyclic order.



Figure 1-23. LED Toggling Rate on a Serial Terminal

💆 COM27 - Tera Term VT	—	×
File Edit Setup Control Window Help		
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While the LED toggling rate on the Serial Terminal changes with every subsequent switch press, observe the same change in the toggling rate of the LED0 on the evaluation kit.



TB3353 Resources

### 2. Resources

- PIC32CM JH01 Curiosity Pro Evaluation Kit
- For additional information on MPLAB Harmony v3, refer to the Microchip web site: https://www.microchip.com/mplab/mplab-harmony and

microchipdeveloper.com/harmony3:start

- For more information on various applications, refer to: github.com/Microchip-MPLAB-Harmony/reference\_apps
- For the example application, refer to "Getting Started Application with PIC32CM JH01 Curiosity Pro Development Board" under the "Software" heading. www.microchip.com/en-us/development-tool/ev81x90a
- For additional info about 32-bit Microcontroller Collaterals and Solutions, refer to: ww1.microchip.com/downloads/aemDocuments/documents/MCU32/ProductDocuments/ ReferenceManuals/32-bit-Microcontroller-Collateral-and-Solutions-Reference-Guide-DS70005534.pdf



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