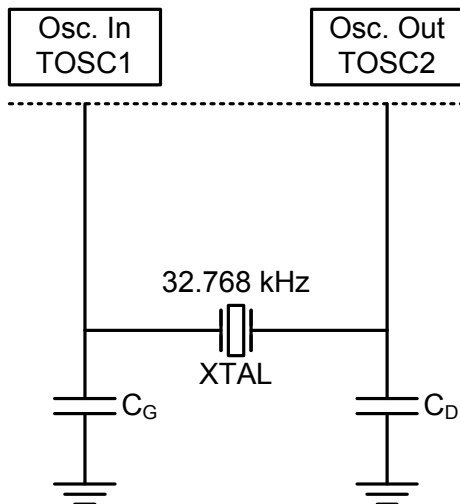


Pierce Oscillator

Design and Crystal
Recommendations

Atmel
ATmega164PA

ATmega164PA



Oscillator Design Check

Test Conditions			
Power Supply Voltage V_{DD}	1.8	5.5	V
Load Capacitors C_D / C_G	5.6 / 5.6		pF
Results			
Effective Load Capacitance	7.4	6.9	pF
Oscillation Allowance	445	500	k Ω
Oscillator Output Voltage AC	295	300	mV _{RMS}
Drive Level	0.065	0.055	μ W
Startup Time	500	400	ms
Overtone Mode Suppression	Safe		----

Recommendation

Crystal		
Crystal Type	MS3V-T1R / CC7V-T1A	
Frequency	32.768	kHz
Load Capacitance C_L	7.0	pF
Tolerance	+/-20	ppm
Oscillator Design		
C_D	5.6	pF
C_G	5.6	pF

Remarks

The ATmega164PA consists of a self limiting Pierce Oscillator.

Placing $C_D = 5.6$ pF and $C_G = 5.6$ pF load capacitors on each side of the crystal results in an effective load capacitance of 7.4 pF at $V_{DD} = 1.8$ V and 6.9 pF at $V_{DD} = 5.5$ V (including board stray capacitances) which are perfect matches for a crystal specified for $C_L = 7.0$ pF.

The oscillator circuit provides an oscillation allowance of 445 k Ω at $V_{DD} = 1.8$ V and 500 k Ω at $V_{DD} = 5.5$ V; this allows the safe use of smallest SMD quartz crystals (ESR ≤ 90 k Ω).

Date: October 2010

Revision N°: 1.0

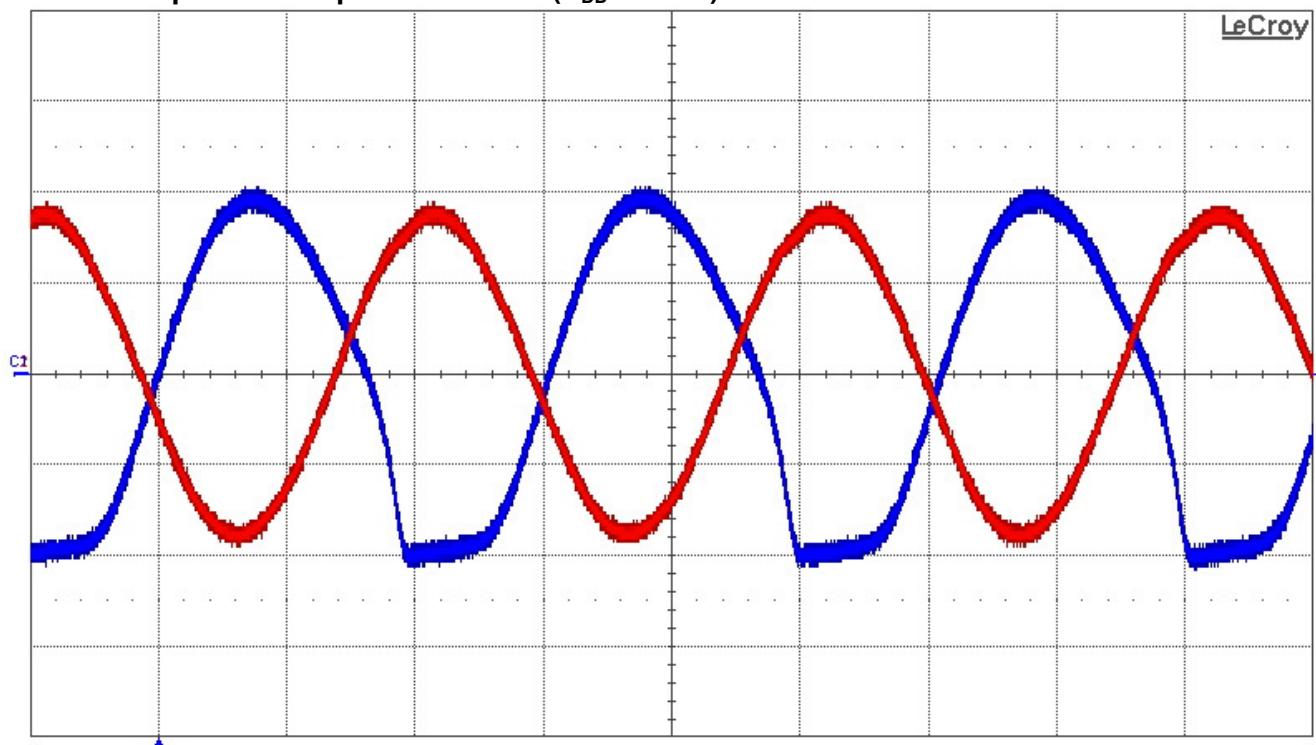
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In accordance with our policy of continuous development and improvement,
Micro Crystal reserves the right to modify specifications or design-recommendations without prior notice.
The recommendations stated above are based on measured-results, respecting the "oscillator design rules".
Micro Crystal makes no representation or warranty for information in this "Design and Crystal Recommendations".

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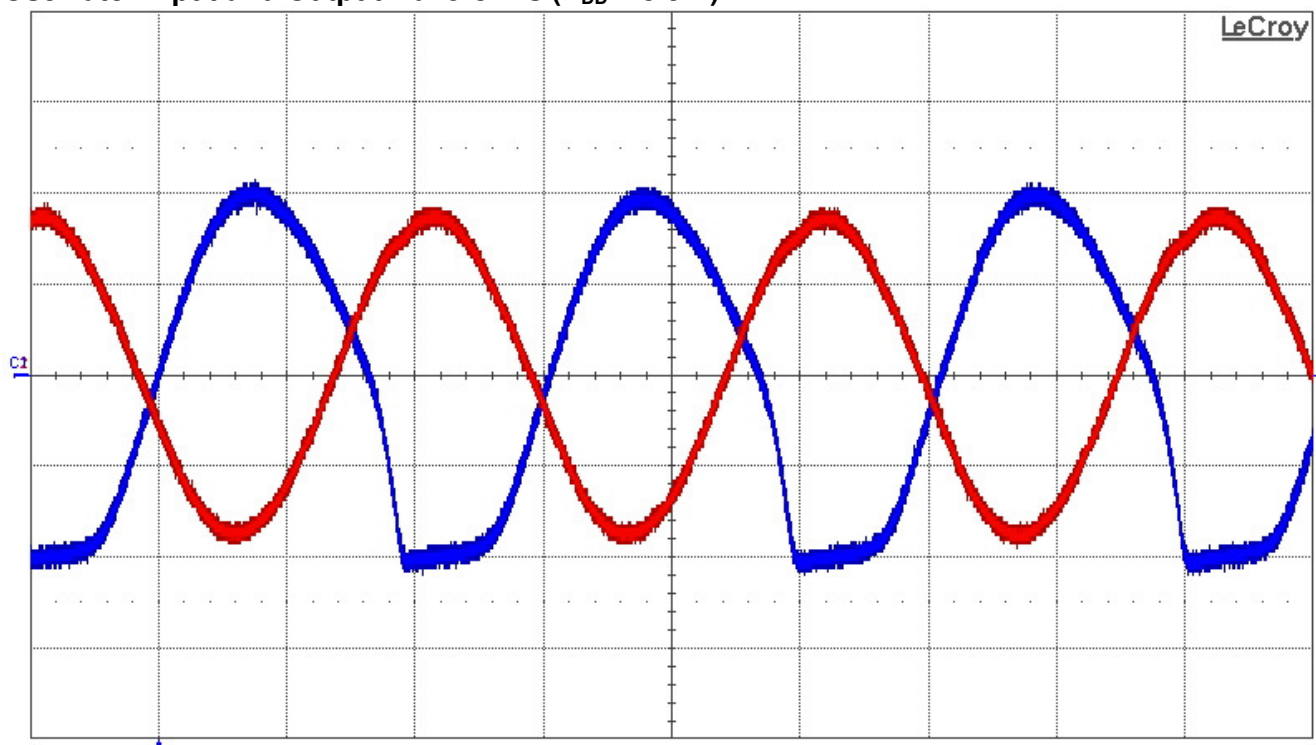
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Oscillator Input and Output waveforms ($V_{DD} = 1.8\text{ V}$):



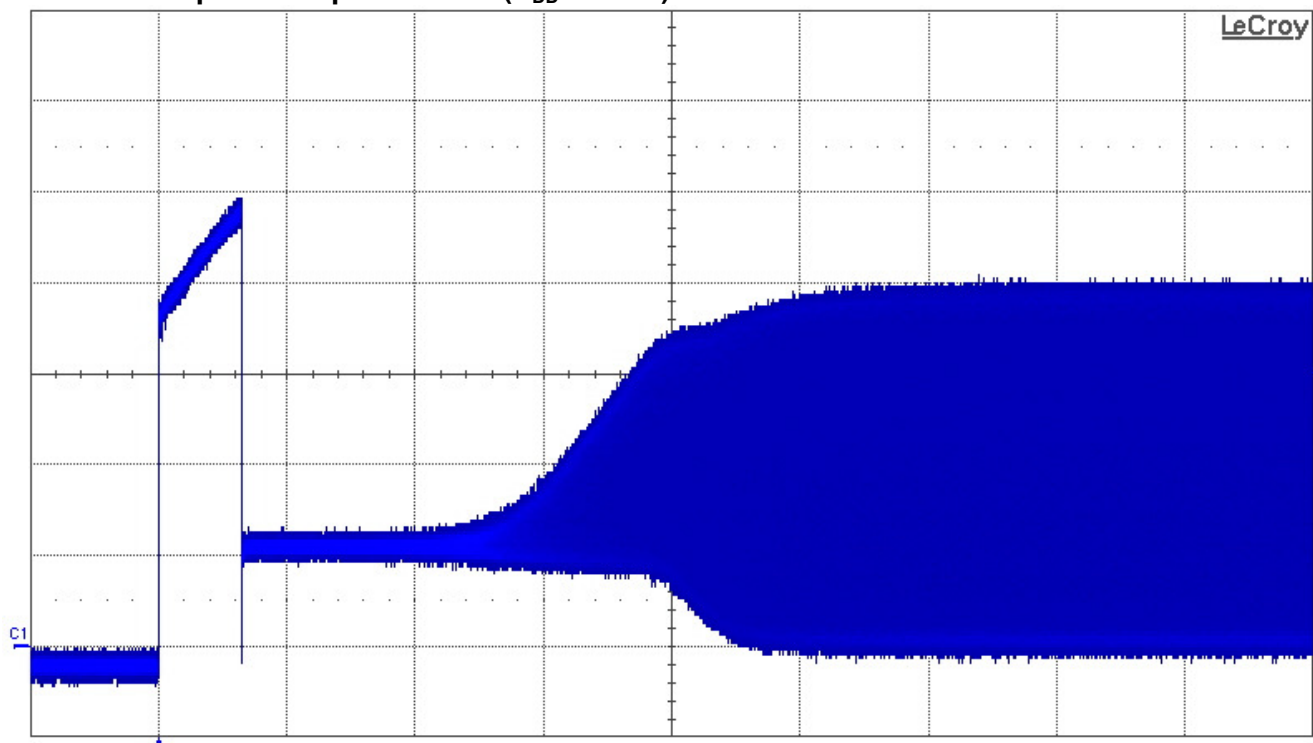
■ C1: Osc. Out (200 mV/div - AC) ■ C2: Osc. In (200 mV/div - AC) Time base: 10 $\mu\text{s}/\text{div}$

Oscillator Input and Output waveforms ($V_{DD} = 5.5\text{ V}$):



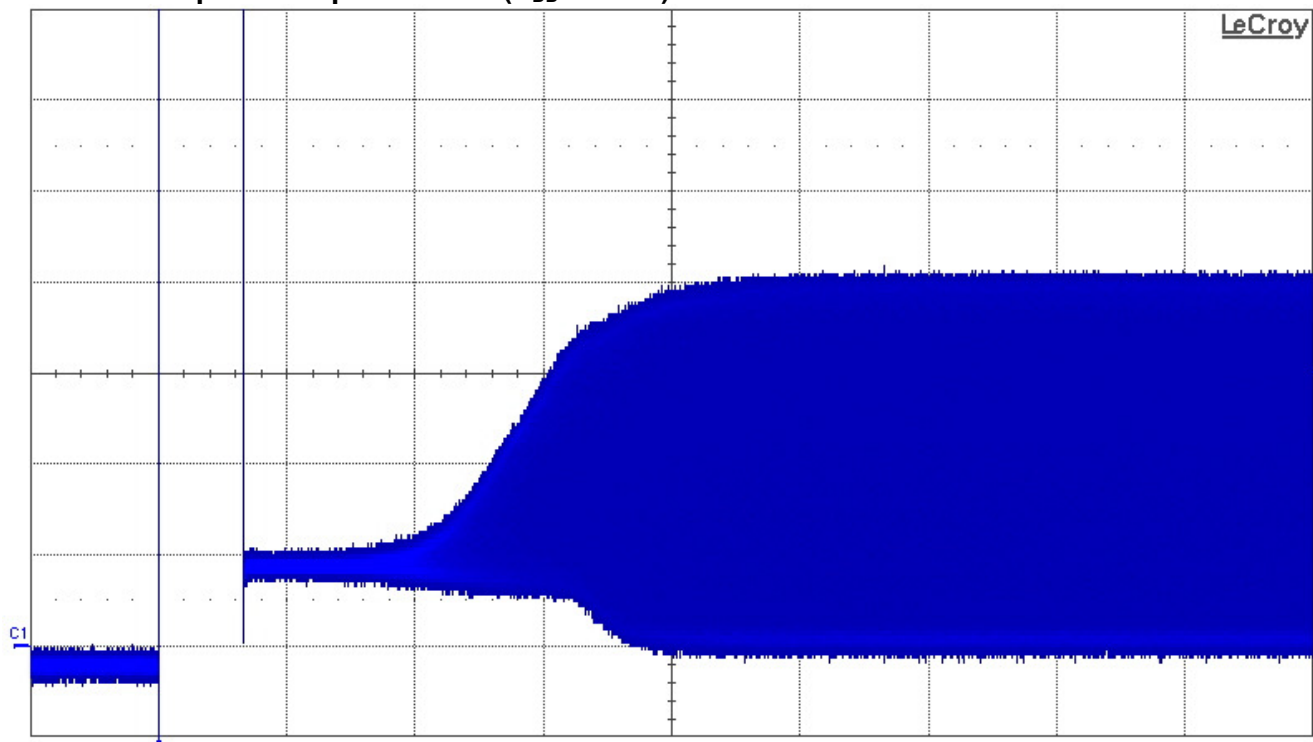
■ C1: Osc. Out (200 mV/div - AC) ■ C2: Osc. In (200 mV/div - AC) Time base: 10 $\mu\text{s}/\text{div}$

Oscillator Output startup waveform ($V_{DD} = 1.8\text{ V}$):



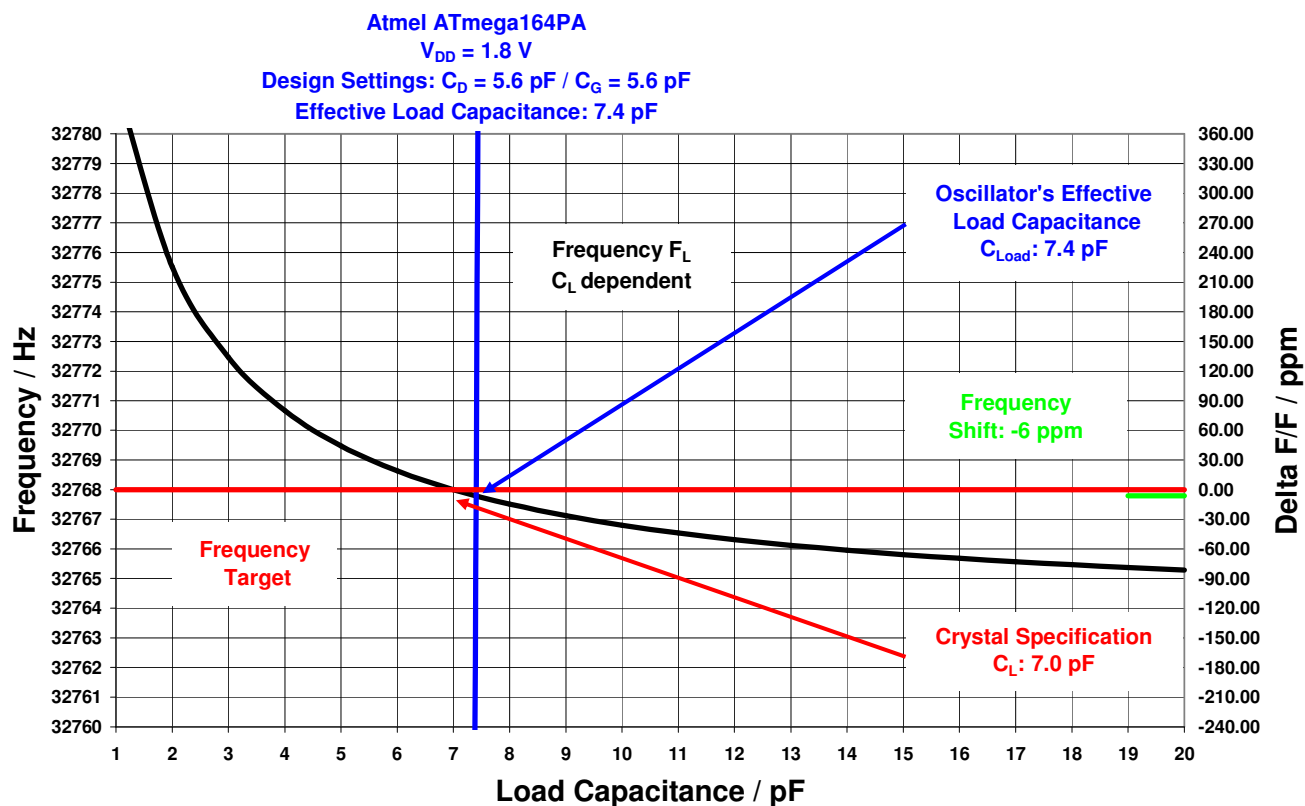
■ C1: Osc. Out (200 mV/div - DC) Time base: 100 ms/div

Oscillator Output startup waveform ($V_{DD} = 5.5\text{ V}$):



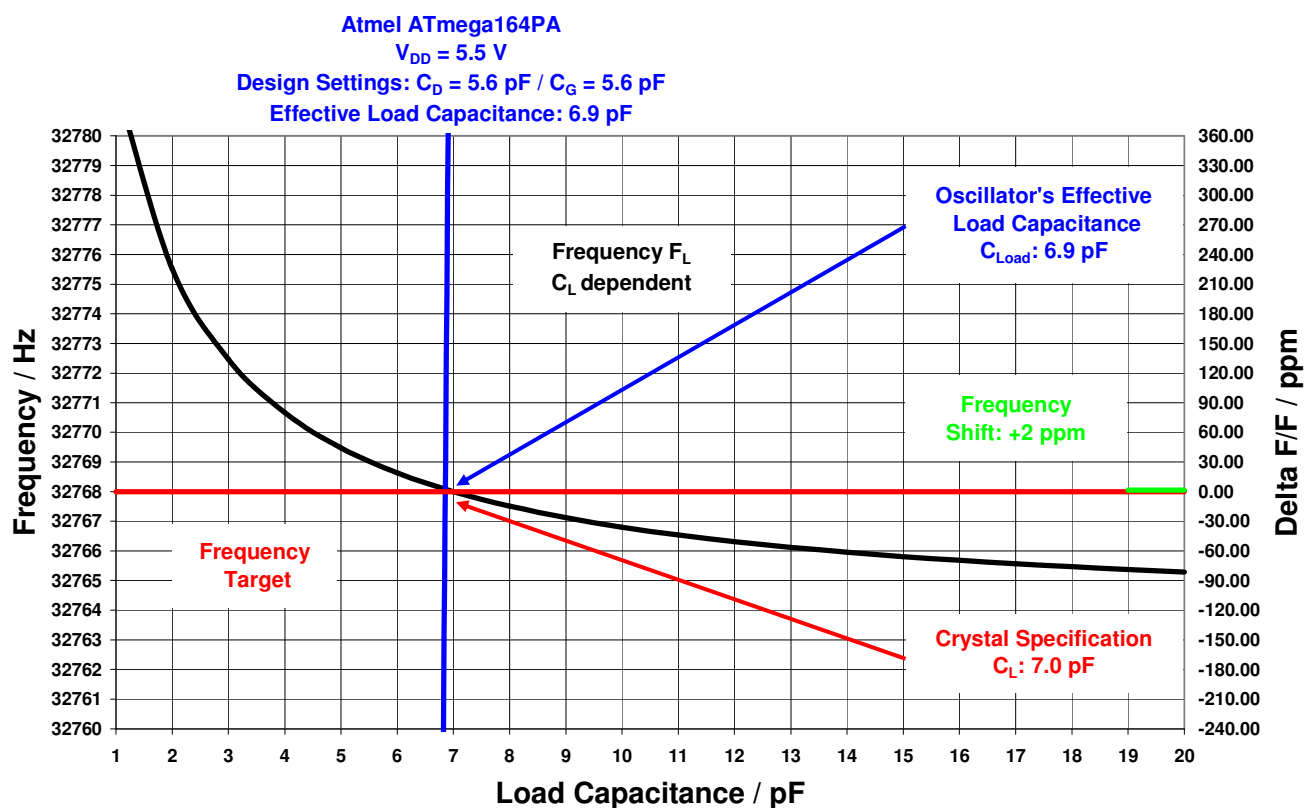
■ C1: Osc. Out (200 mV/div - DC) Time base: 100 ms/div

Crystal matching chart ($V_{DD} = 1.8\text{ V}$):



As shown in the chart above, an effective load capacitance of 7.4 pF results in a frequency offset of -6 ppm using a MS3V-T1R crystal specified for $C_L: 7.0\text{ pF}$.

Crystal matching chart ($V_{DD} = 5.5 \text{ V}$):



As shown in the chart above, an effective load capacitance of 6.9 pF results in a frequency offset of +2 ppm using a MS3V-T1R crystal specified for $C_L = 7.0 \text{ pF}$.