

54SX Family FPGAs RadTolerant and HiRel Errata

In Table 1 on page 8, the Maximum Input Tolerance for the A54SX16 and A54SX32 devices is incorrect.

Here is how the table should appear.

Table 1 • Supply Voltages

Devices	V _{CCA}	V _{CCI}	V _{CCR}	Maximum Input Tolerance	Maximum Output Drive
A54SX16 A54SX32	3.3V	3.3V	5.0V	5.0V	3.3V
RT54SX16 RT54SX32	3.3V	3.3V	5.0V	5.0V	3.3V

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